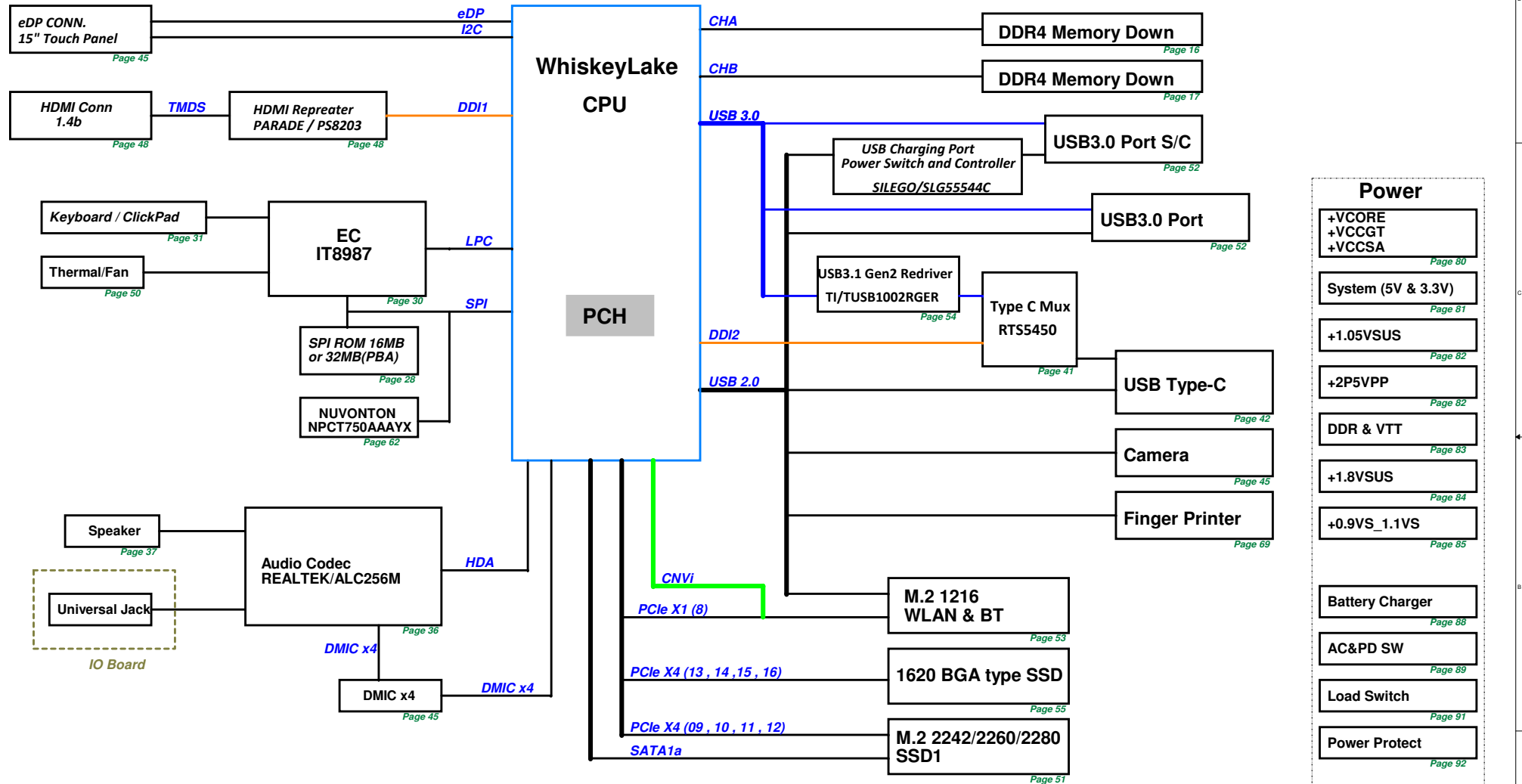


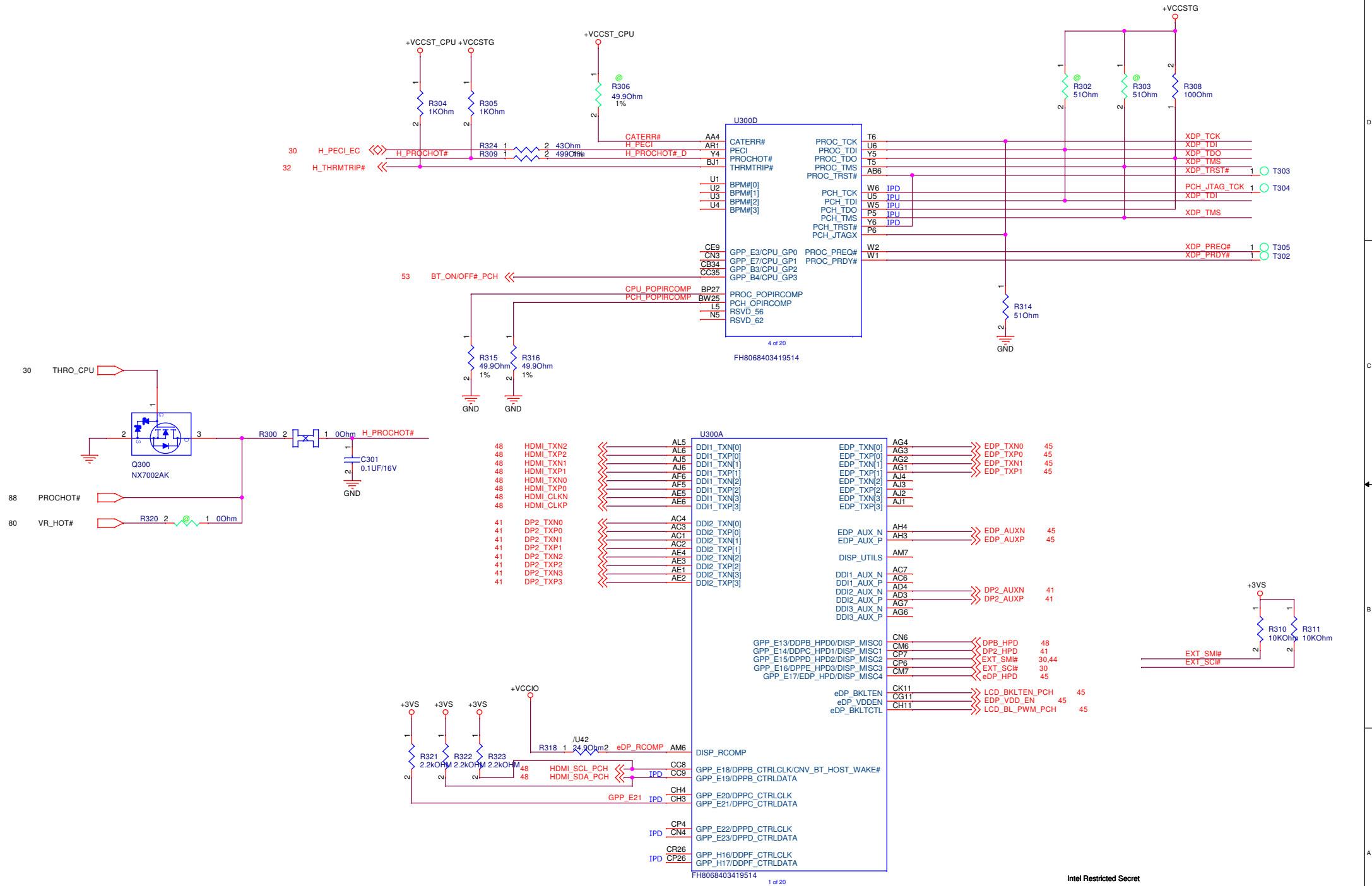
# Guinness\_WL R2.1

## Block Diagram



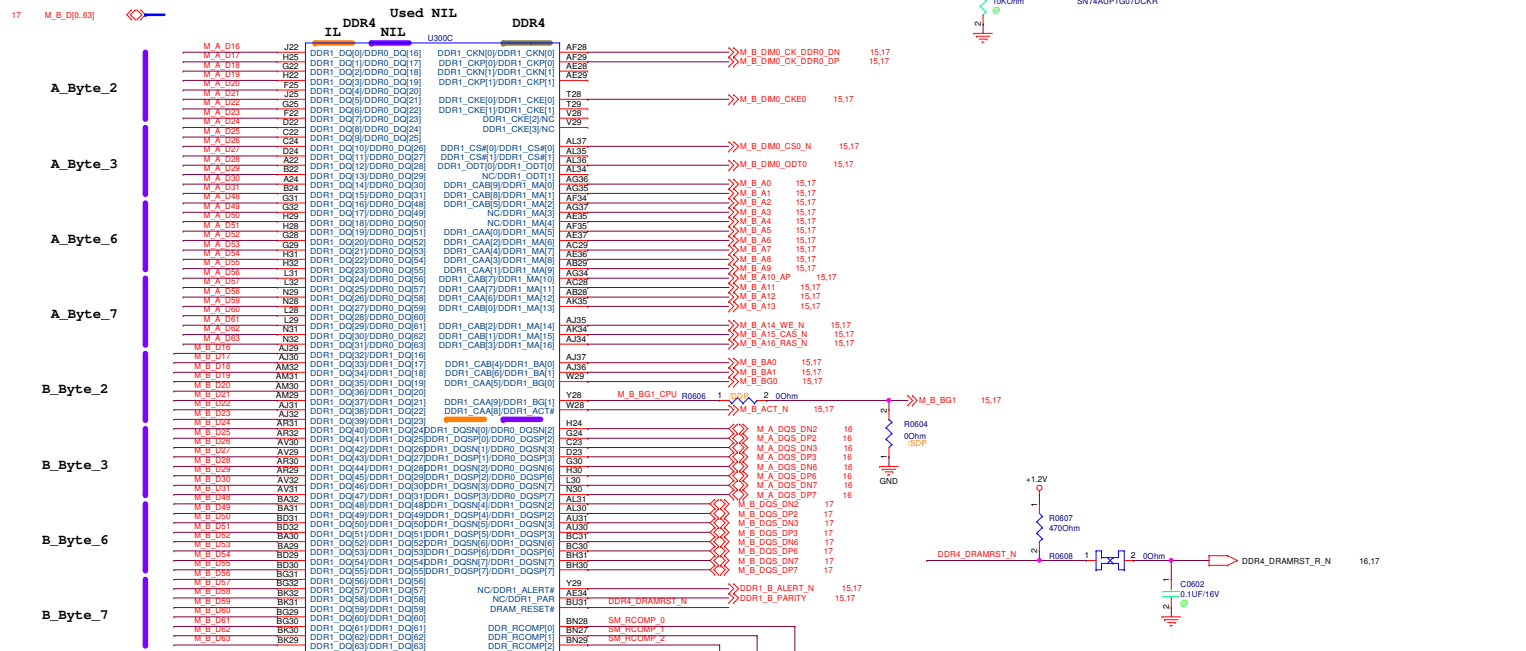
# Option

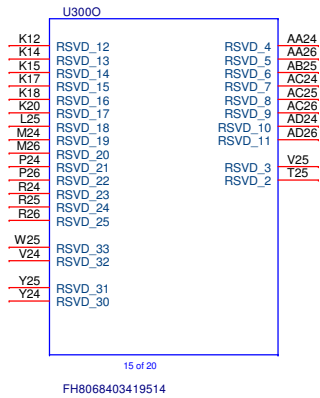
Optional	Remark
@	Ummount
/Debug	Debug only
/EMI	Reserved EMI part
/TPM	Support TPM function
/U22	Support 2+2 CPU
/U42	Support 4+2 CPU
/U42-DDP	
/U42-SDP	
/CHA-DIS	
/CHA-ON	
/CHB-DIS	
/CHB-ON	
/CHB	
/DDP	
/SDP	
/CNV	
/PCIE_WLAN	
/2MIC	
/4MIC	
/BL	
/USBSLP	
/MS	
/MS_CPU_C10_GATE#	
/MS_SLP_S0#	
/Non-MS	
/SSD_HYNIX	
/SSD_YOSUN	
/TEST	

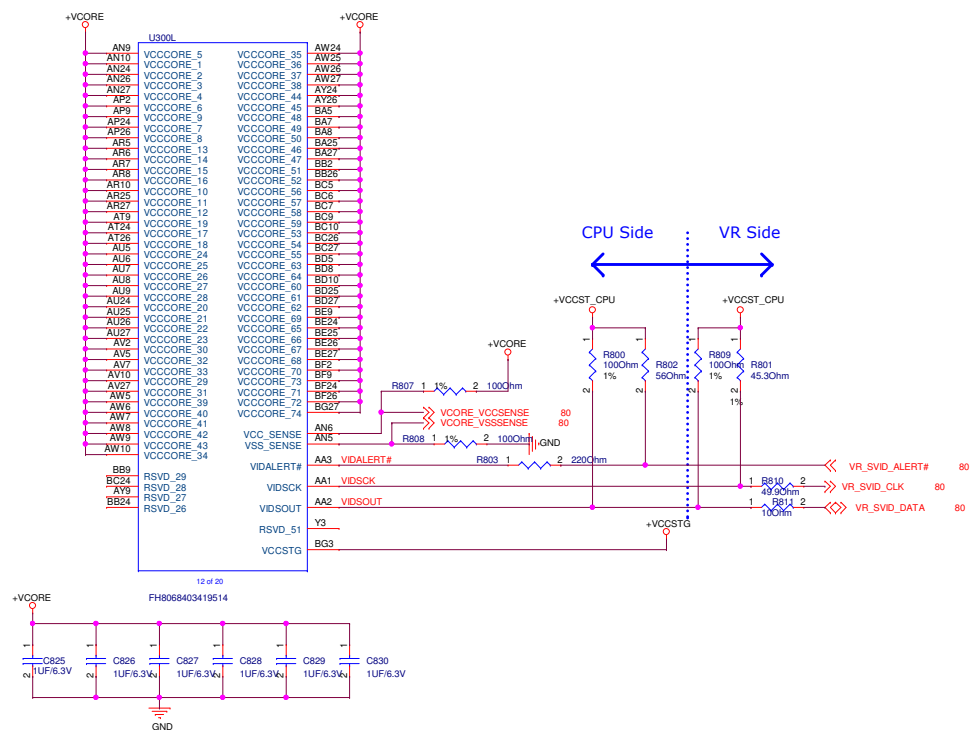
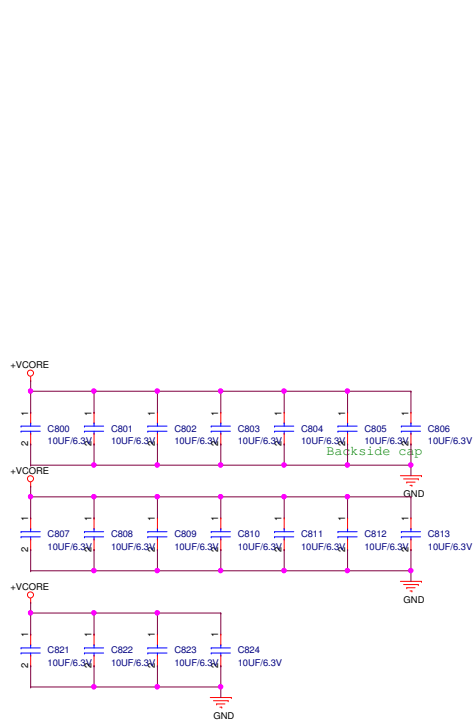




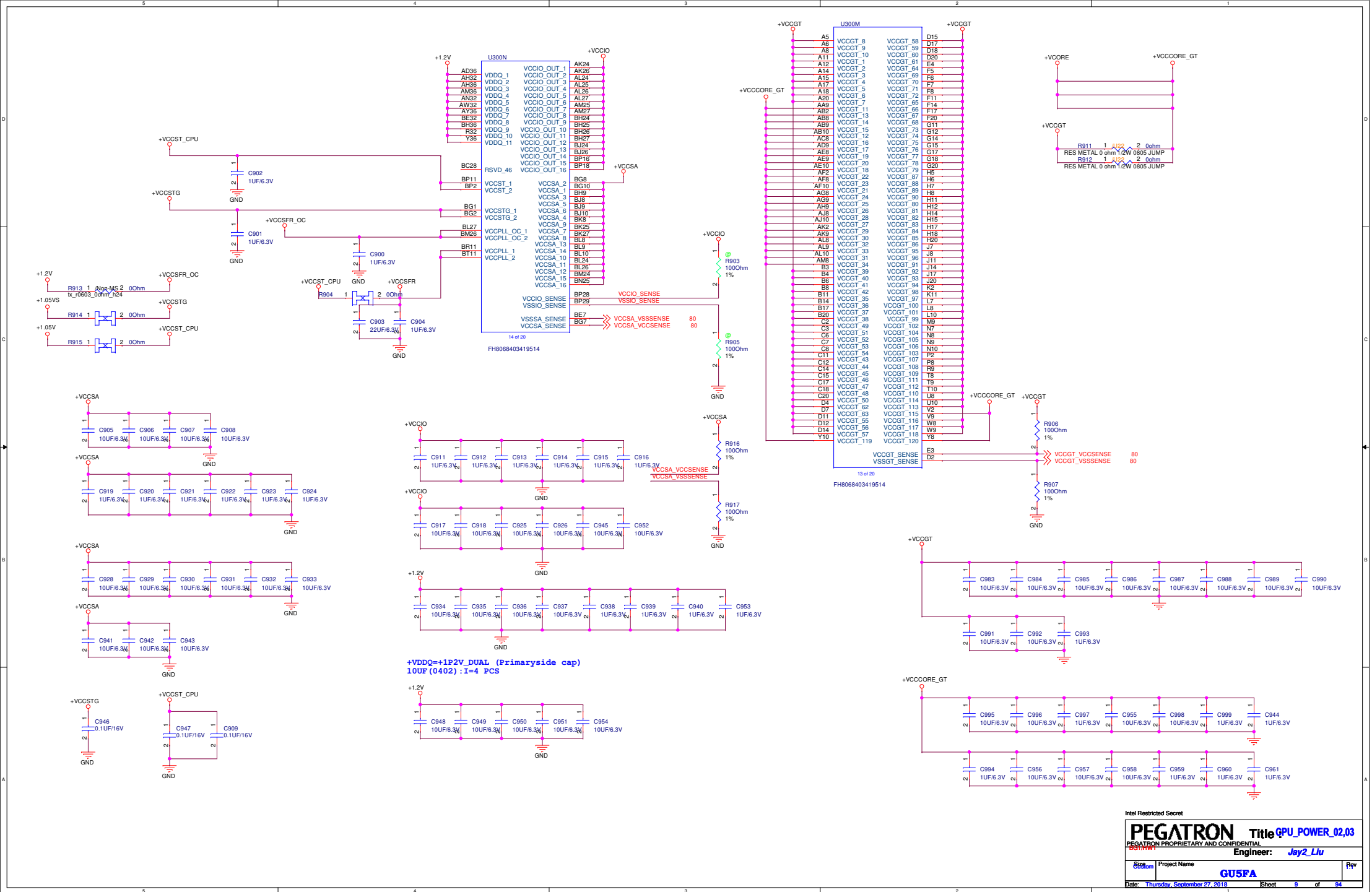


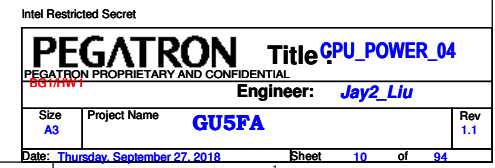


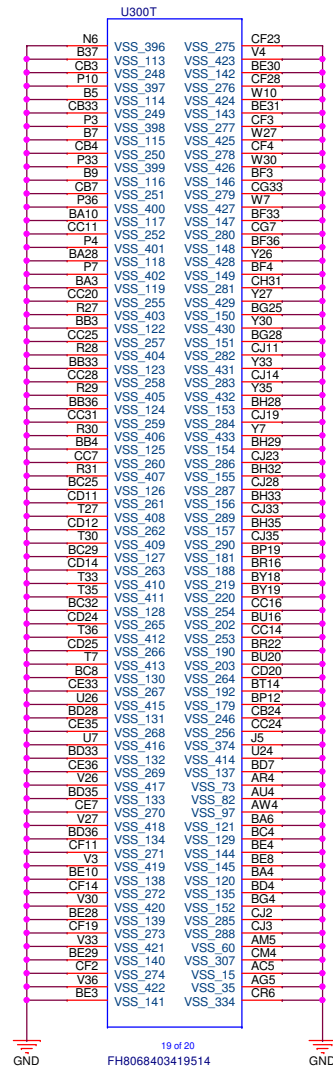
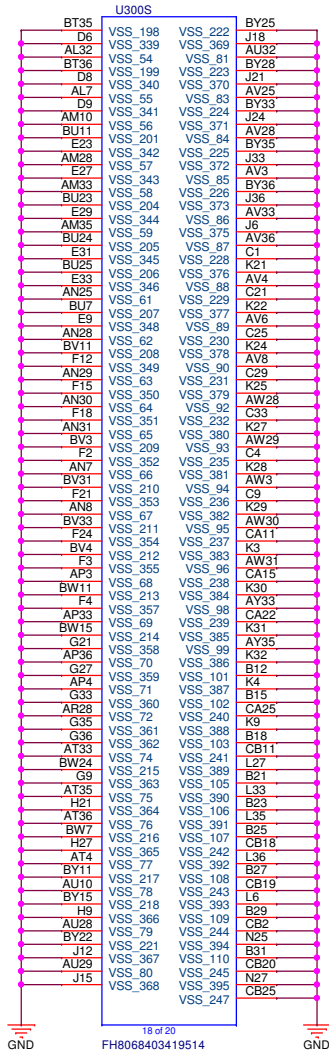
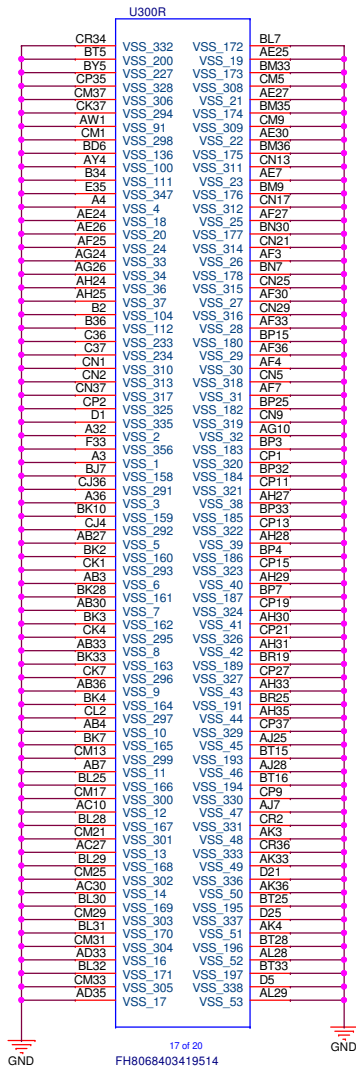












Intel Restricted Secret

<b>PEGATRON</b> Title: <b>GPU_GND</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL	
Engineer: <b>Jay2_Liu</b>	
Size <b>A3</b>	Project Name <b>GU5FA</b>
Date: <b>Thursday, September 27, 2018</b>	Sheet <b>11</b> of <b>94</b>
Rev <b>1.1</b>	

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

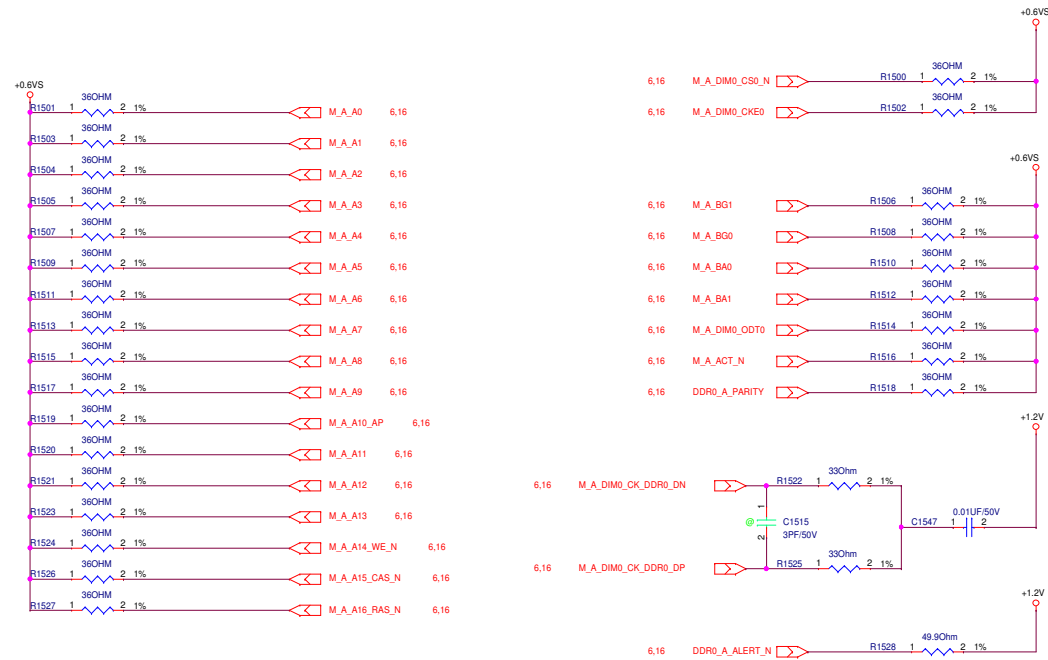
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
12	Thursday, September 27, 2018	94	





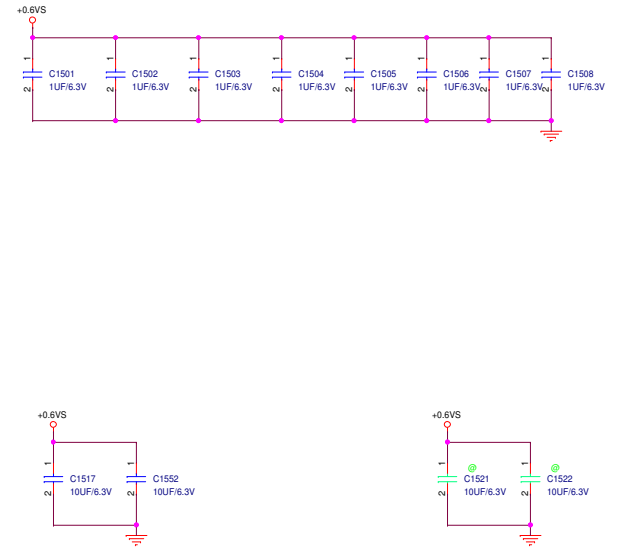
# 15 DDR4(0)\_Termination

CHA

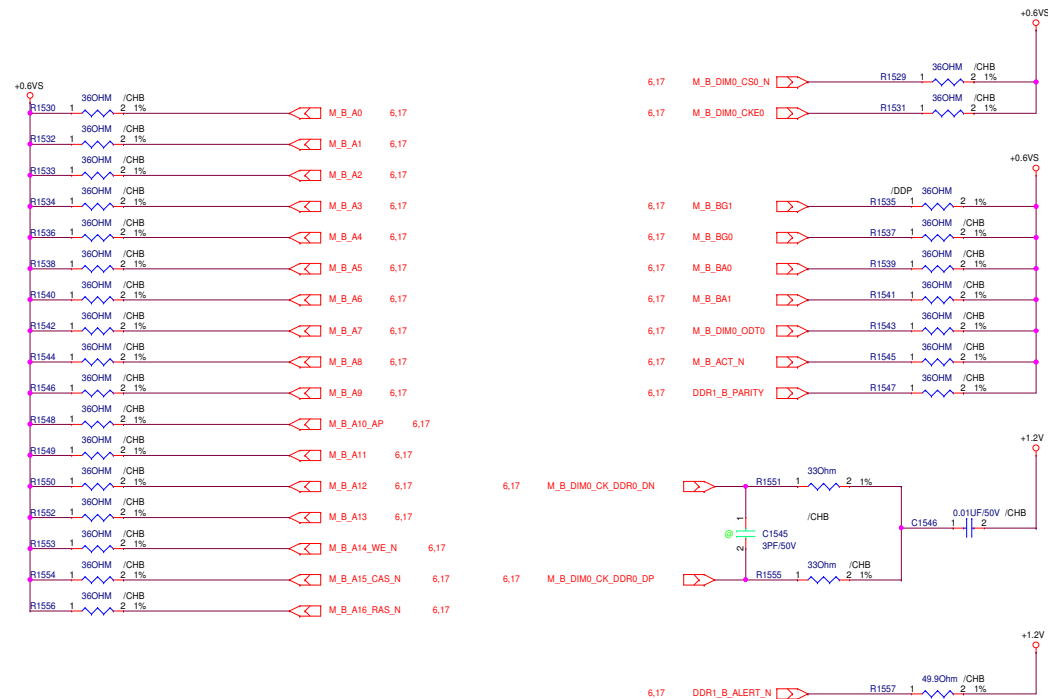


Average placed close to +VDDQ\_VTT power plane

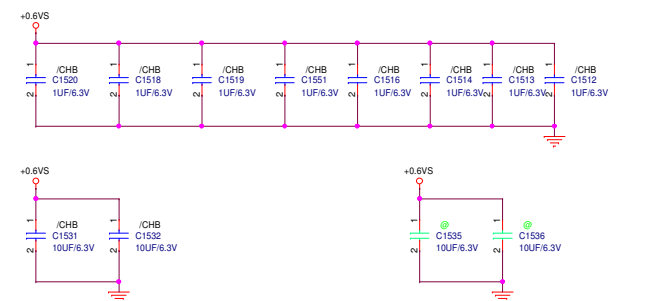
Follow intel PDG #575412 0.7



CHB



Follow intel PDG #575412 0.7



<Variant Name>

**PEGATRON** Title: **DDR4(0)\_Termination**  
RESISTOR PROPRIETARY AND CONFIDENTIAL

Engineer: **Jay2 Liu**

Size	Project Name	<b>GUSFA</b>	Rev
Date	Thursday, September 27, 2018	15	94










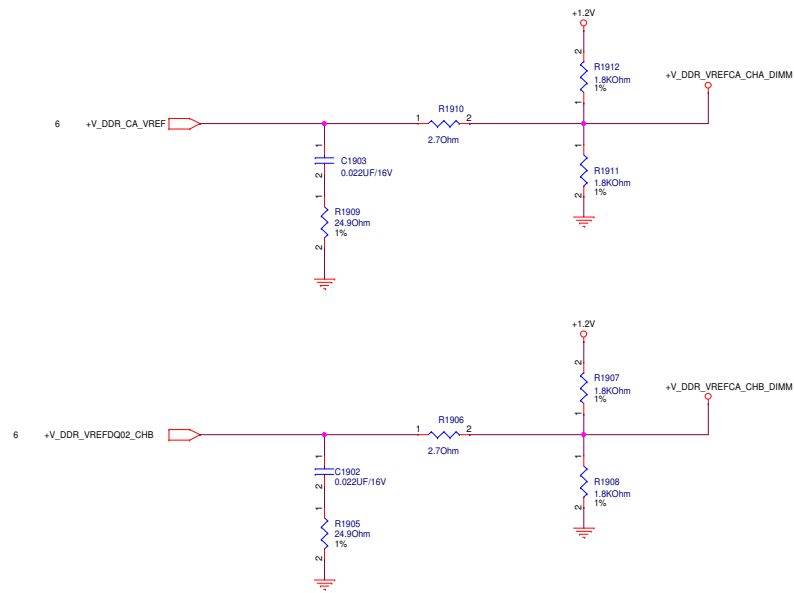
5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

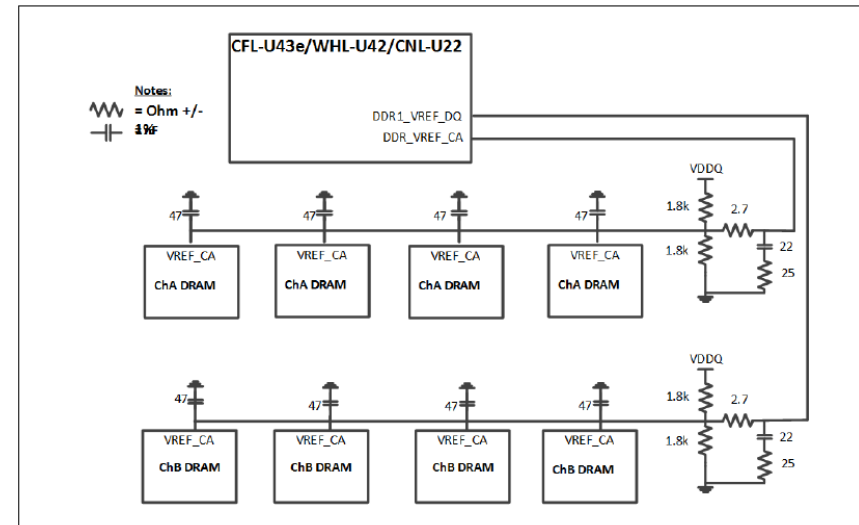
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name		Rev.
	Thursday, September 27, 2018	18	94

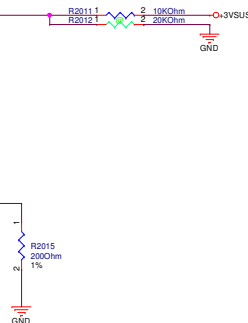
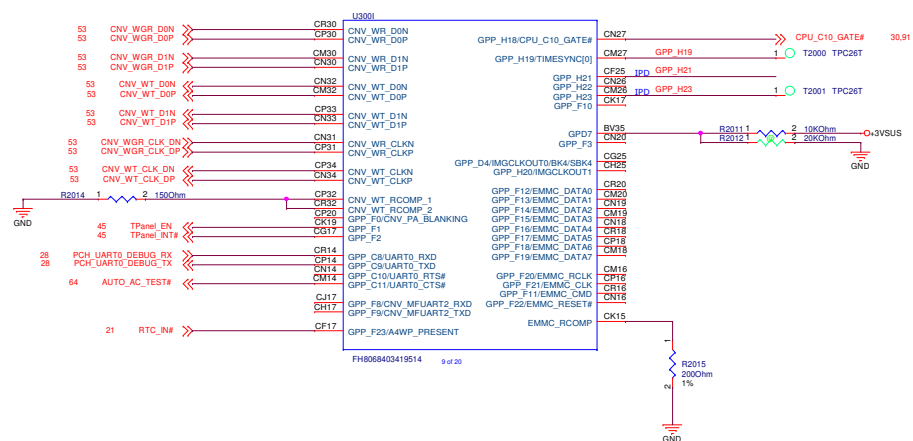
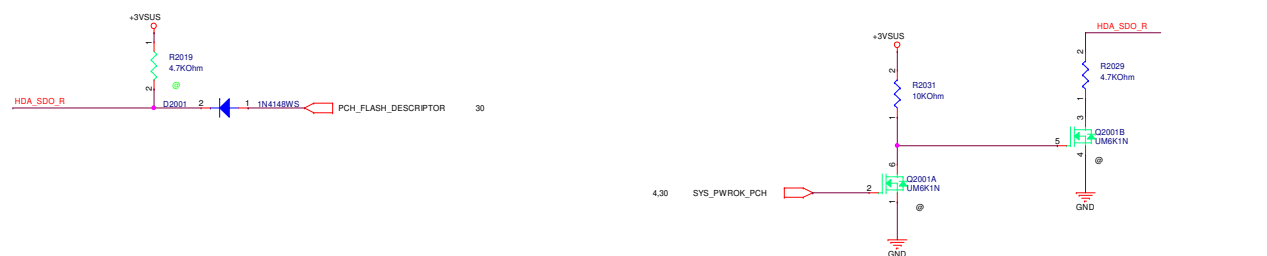
# 19 DDR4(3)\_CA/DQ Voltage

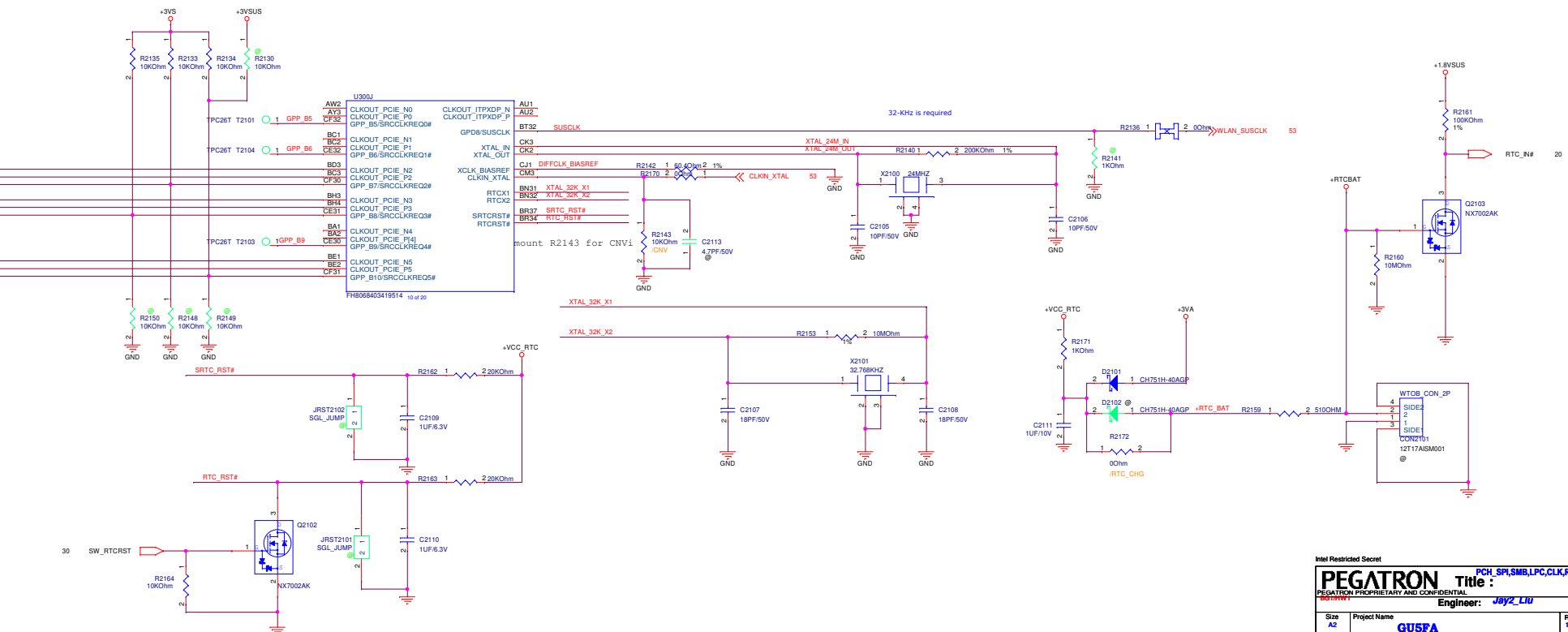
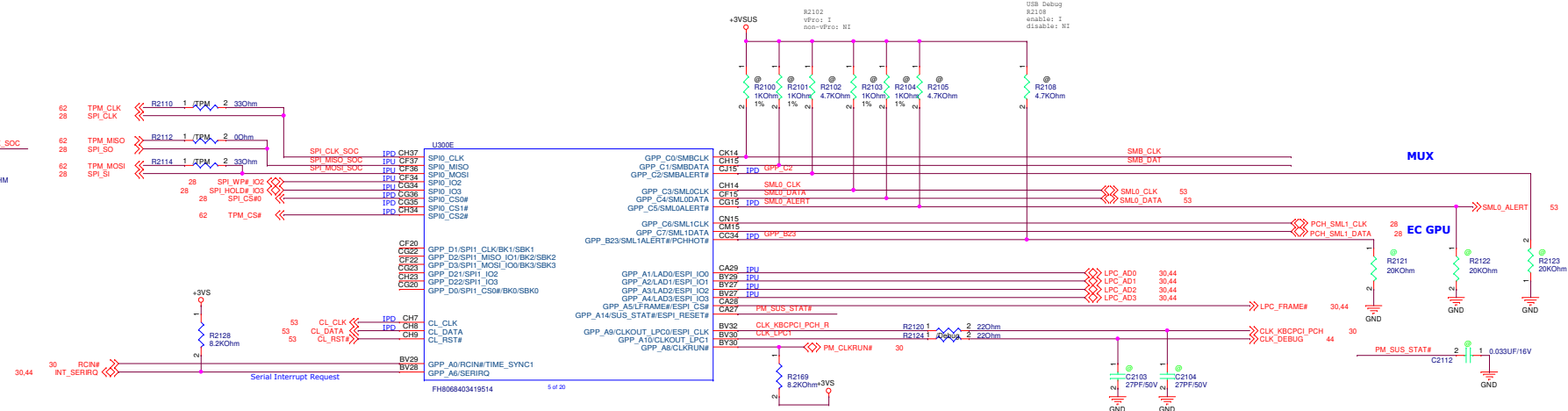
+1.2V		+1.2V	6,9,15,16,17,57,83
+V_DDR_VREFCA_CHB_DIMM		+V_DDR_VREFCA_CHB_DIMM	17
+V_DDR_VREFCA_CHA_DIMM		+V_DDR_VREFCA_CHA_DIMM	16



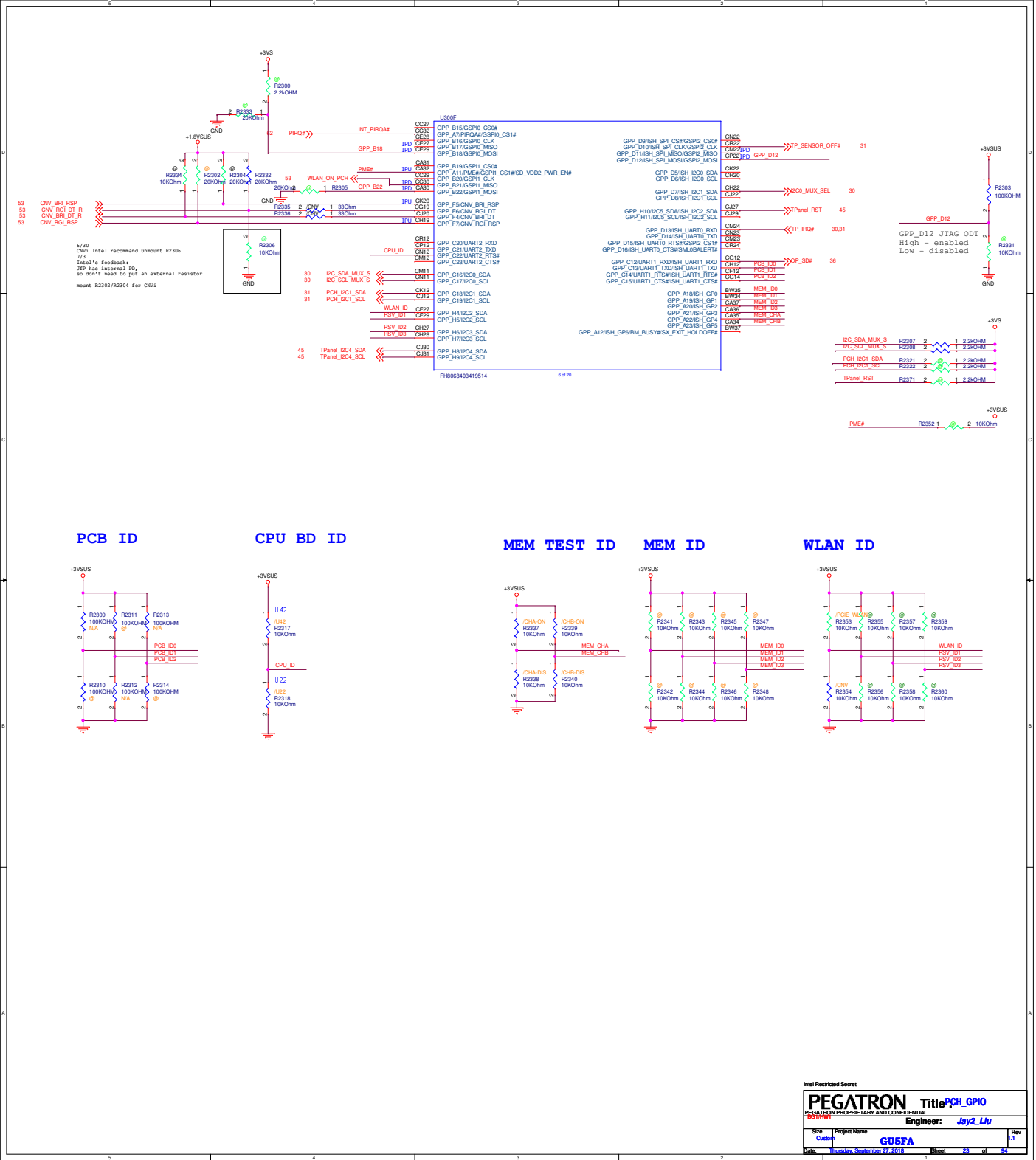
## WHL U DDR4 x16 Devices Memory Down VREF-CA Overview













<Core Design>

PEGATRON

PEGATRON PROPRIETARY AND CONFIDENTIAL

SGT/HWT

Size

C

Project Name

GUSFA

Date:

Thursday, September 27, 2018

Title <Title>

Engineer: Jay2 Liu

Rev

1.1

Sheet

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of

94





<Core Design>

PEGATRON

Size

C

Title <Title>

Project Name

GUSFA

PEGATRON PROPRIETARY AND CONFIDENTIAL

SGT/HWT

Date: Thursday, September 27, 2018

Engineer: Jay2 Liu

Rev

1.1

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<Core Design>

<b>PEGATRON</b>		Title <Title>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
SGT/HWT		Engineer: Jay2 Liu	
Size	Project Name		Rev
C	<b>GUSFA</b>		1.1
Date: Thursday, September 27, 2018		Sheet	26 of 84

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

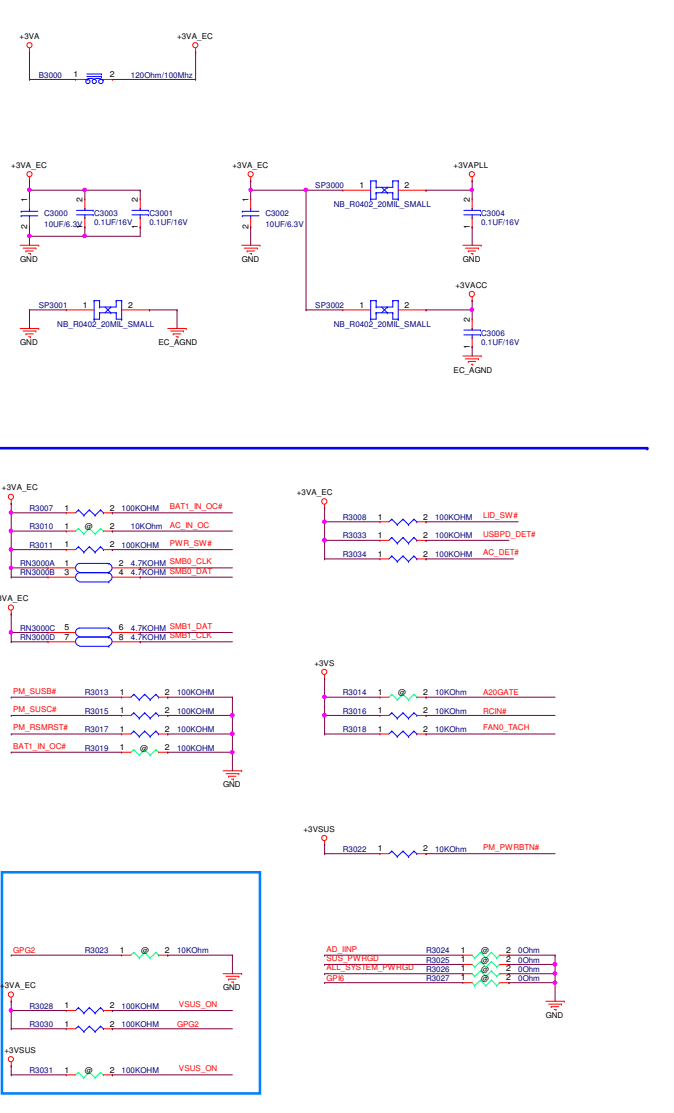
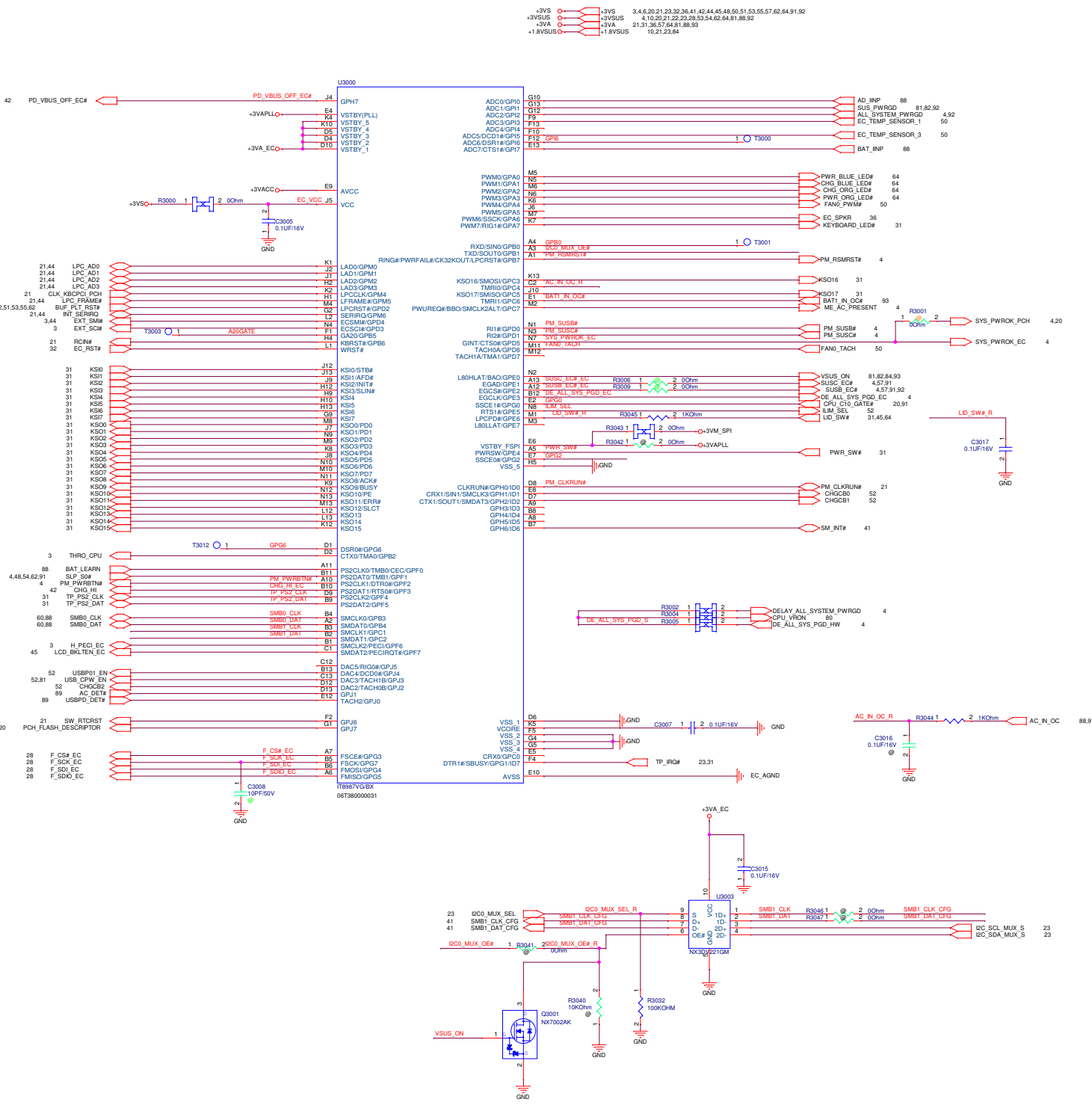
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
456A	Thursday, September 27, 2018	27	94

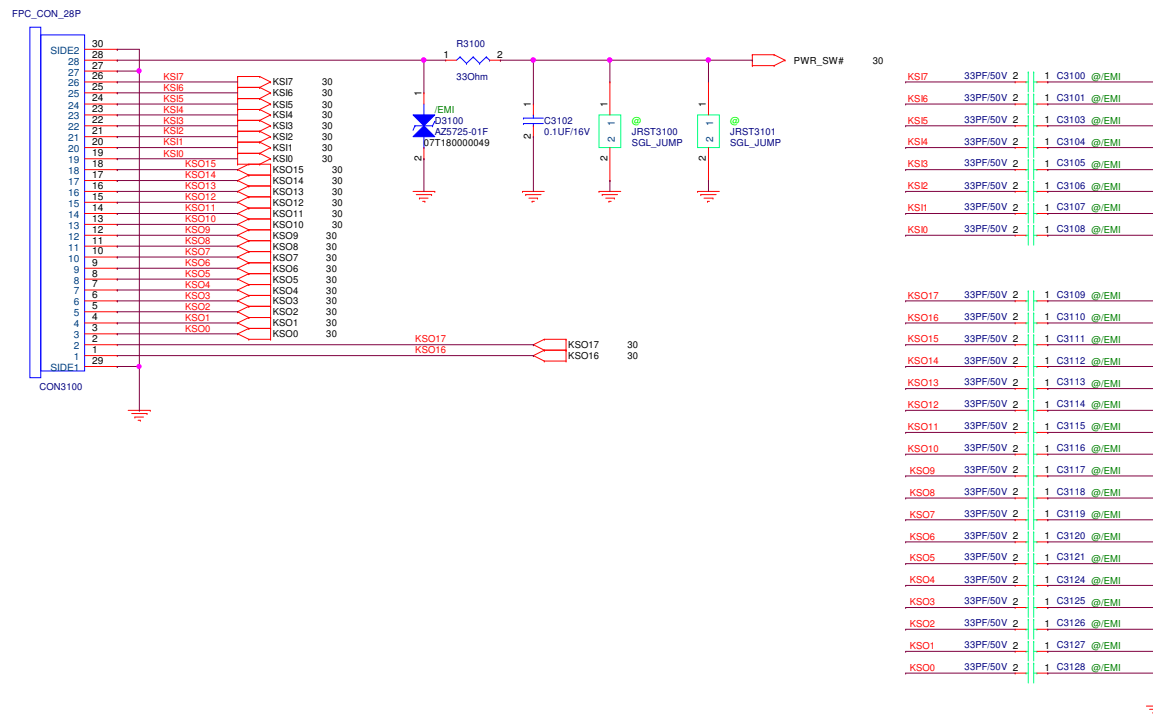


5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

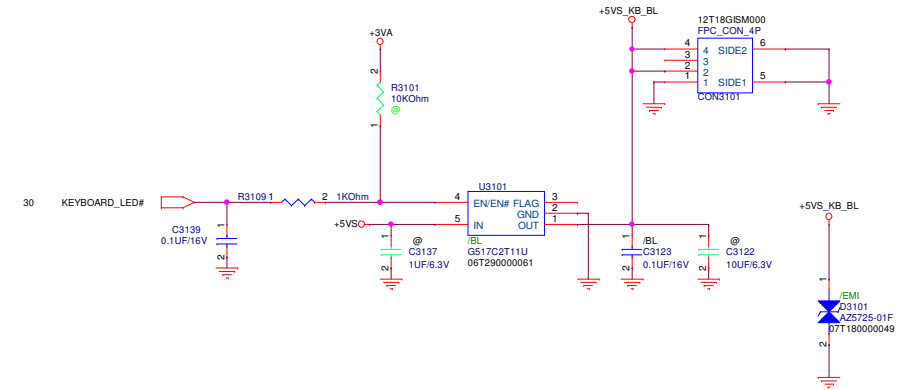
<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
Thursday, September 27, 2018	29	94	

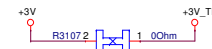
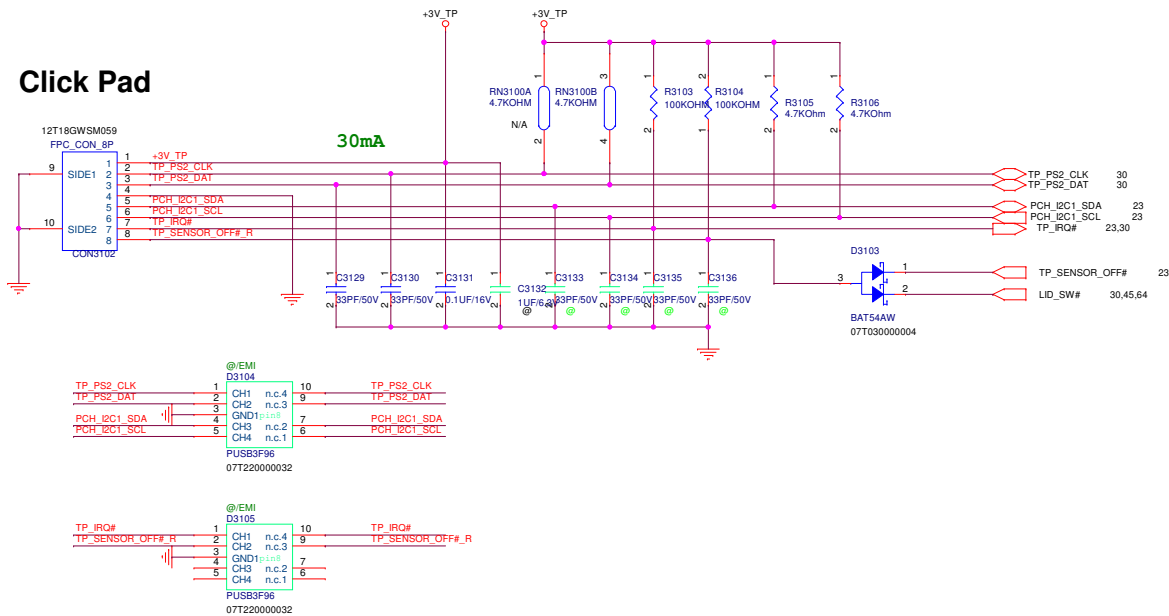


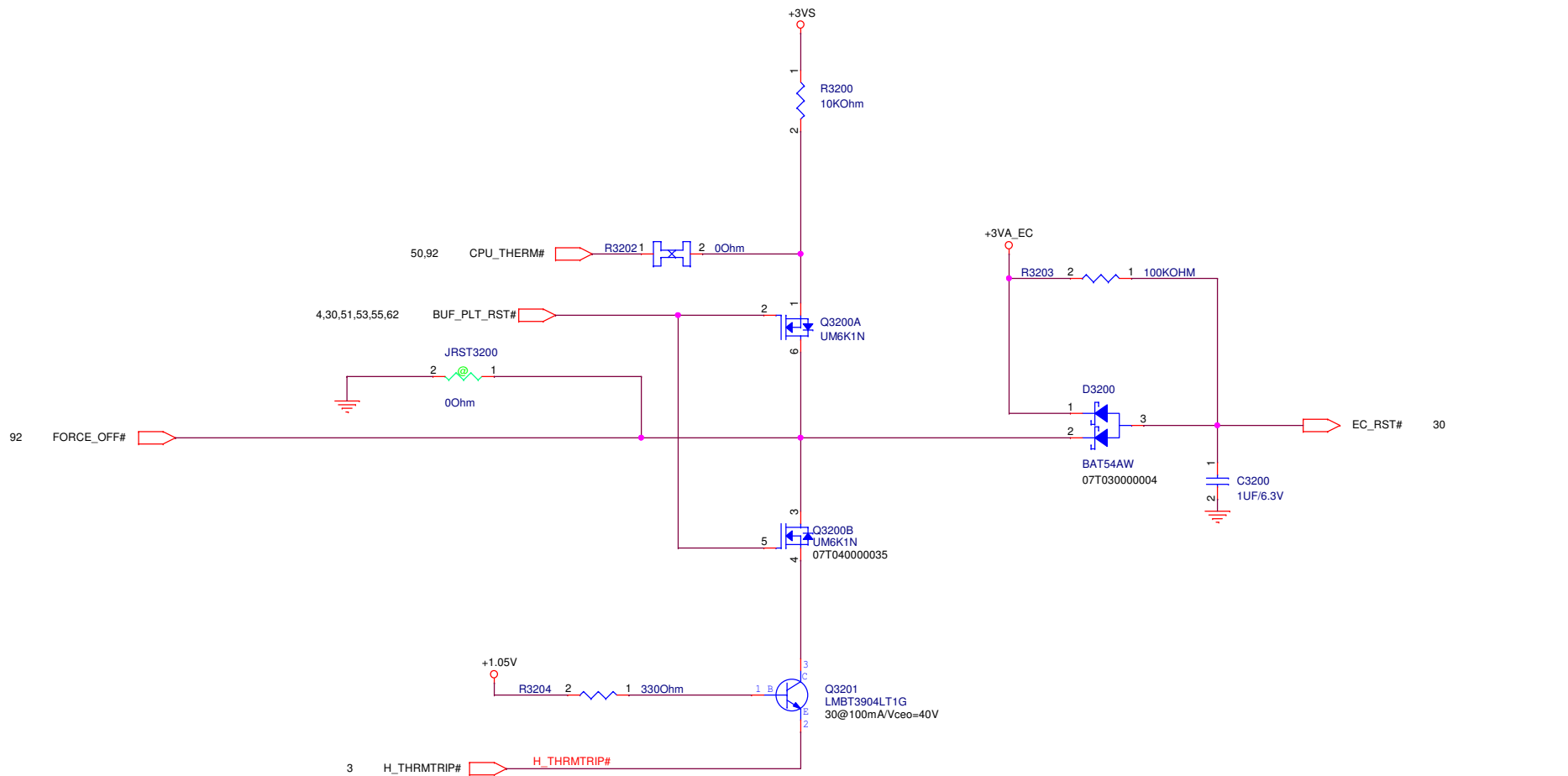


## Keyboard Backlight



## Click Pad







5	4	3	2	1
D				D
C				C
B				B
A				A

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
Thursday, September 27, 2018	33	94	

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
34	Thursday, September 27, 2018	34	94

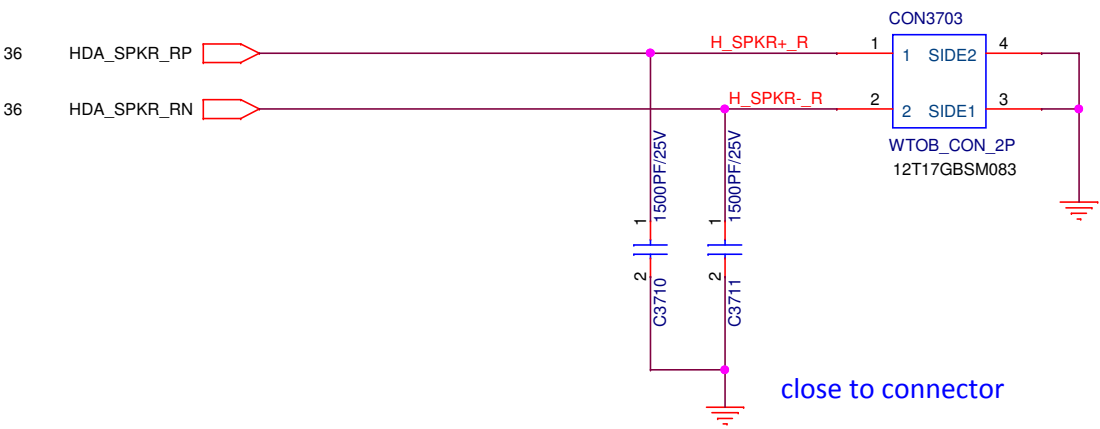
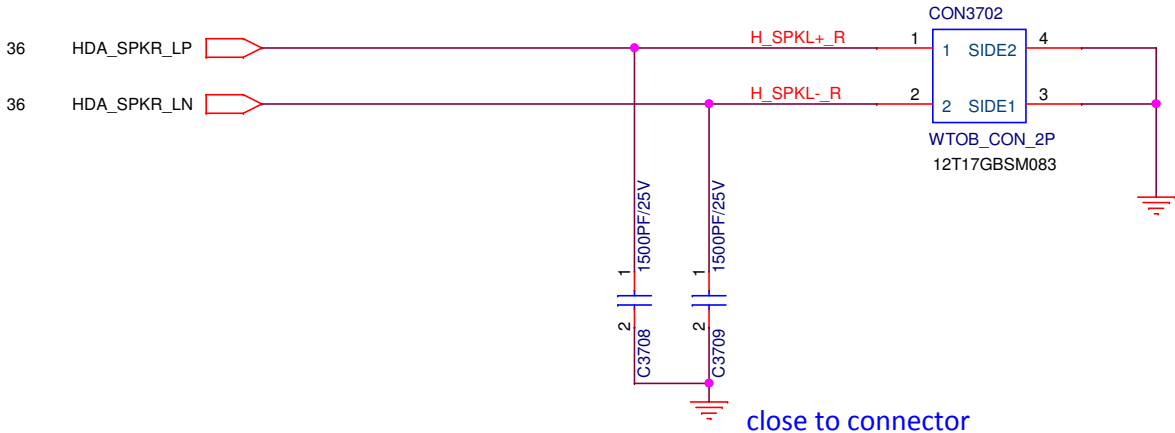
5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
456A	Thursday, September 27, 2018	35	94



# Internal Speaker



<Variant Name>

<b>PEGATRON</b>		Title <b>AUD_HP/DMIC/SPK</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>BG1/HW1</b>		Engineer: <b>Jay2_Liu</b>	
Size Custom	Project Name <b>GU5FA</b>		Rev 1.1
Date: <b>Thursday, September 27, 2018</b>		Sheet <b>37</b> of <b>94</b>	

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
Thursday, September 27, 2018	38	94	

D

C

B

A

<Variant Name>

**PEGATRON** Title :  
PEGATRON PROPRIETARY AND CONFIDENTIAL

PEGATRON PROPRIETARY AND CONFIDENTIAL

**Engineer:**

Size	GPU Subject Name
100	100
200	200
300	300
400	400
500	500
600	600
700	700
800	800
900	900
1000	1000

**Project Name**

Rev1.1

Thursday, September 27, 2018

39

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	Date:

2

Sheet

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1

of

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5

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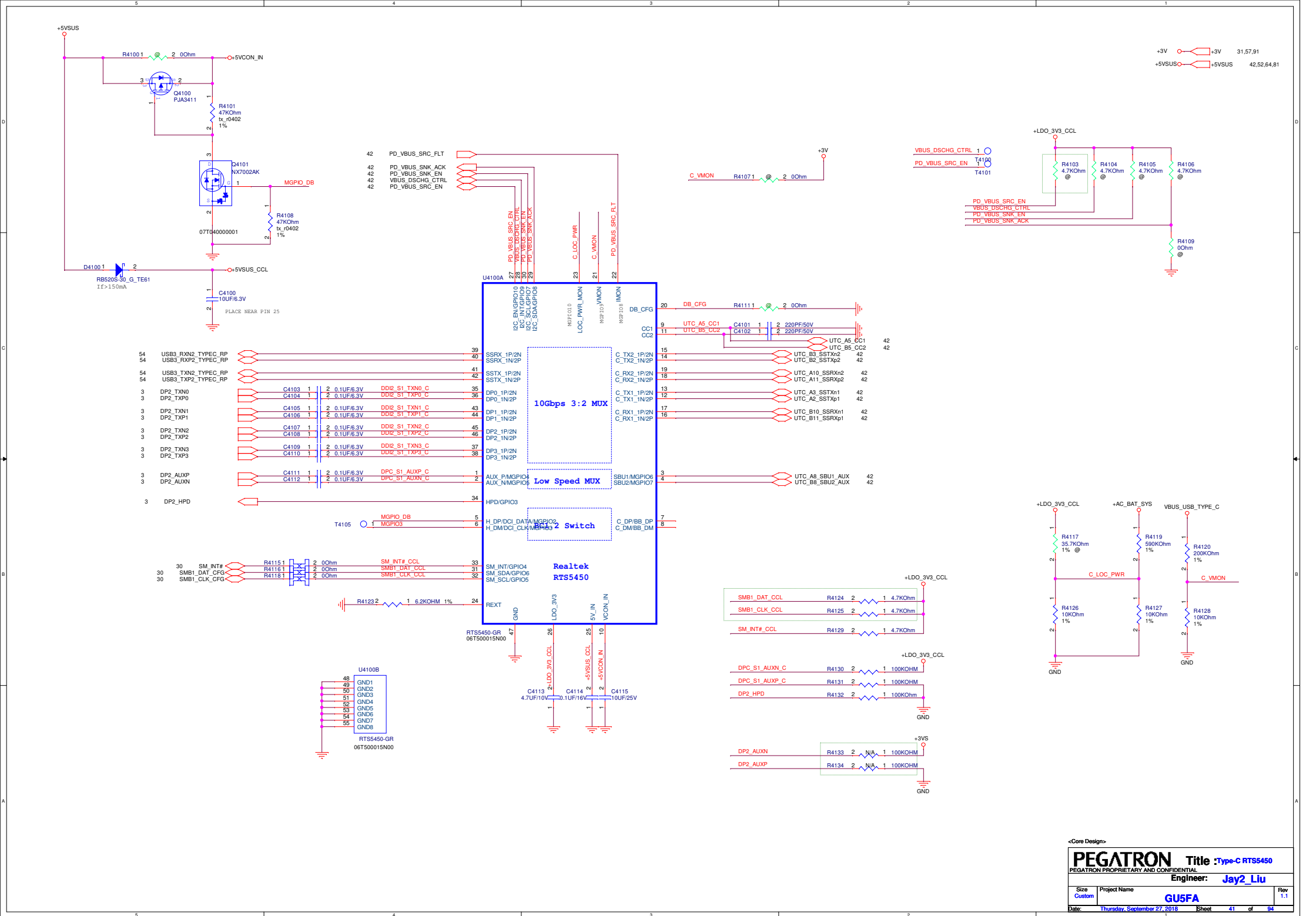
4

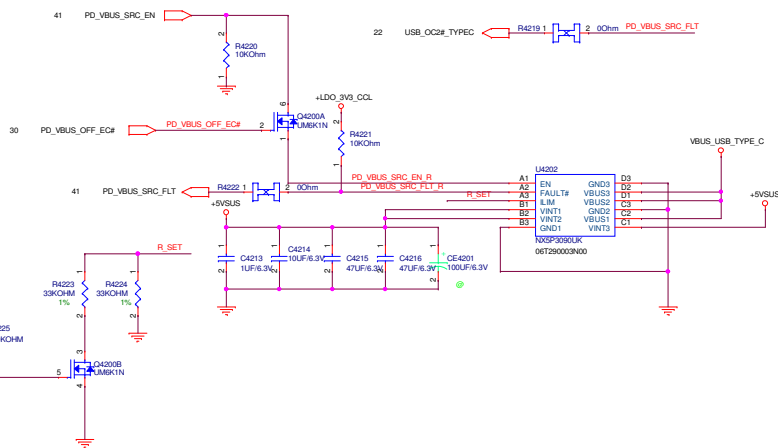
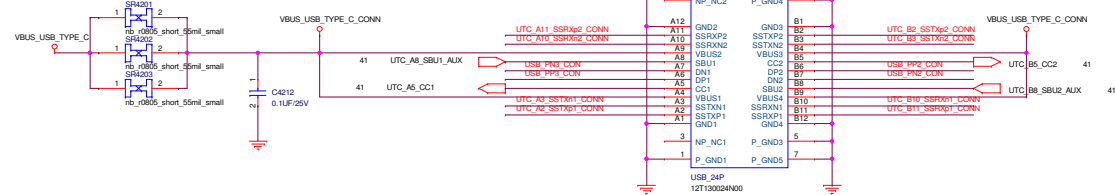
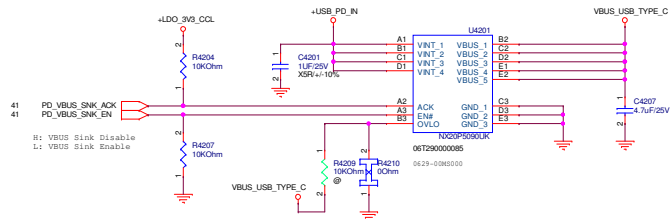
---

3



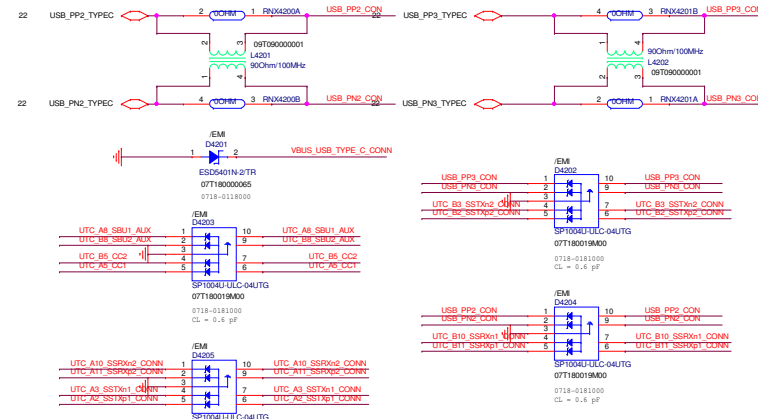
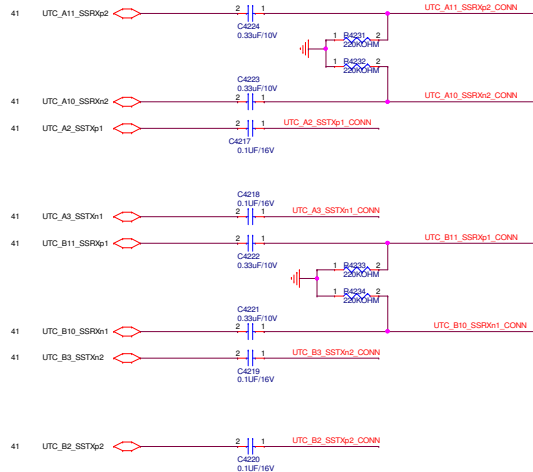
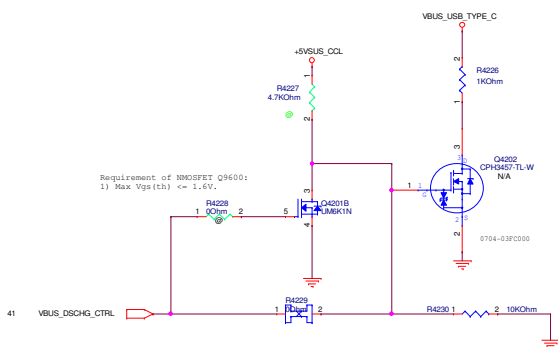






## USB Type C Connector

A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	A11	A12
GND	TX1+	TX1-	Vbus	CC1	D+	D-	SBU1	Vbus	RX2-	RX2+	GND
GND	RX1+	RX1-	Vbus	SBU2	D-	D+	CC2	Vbus	TX2-	TX2+	GND
B12	B11	B10	B9	B8	B7	B6	B5	B4	B3	B2	B1






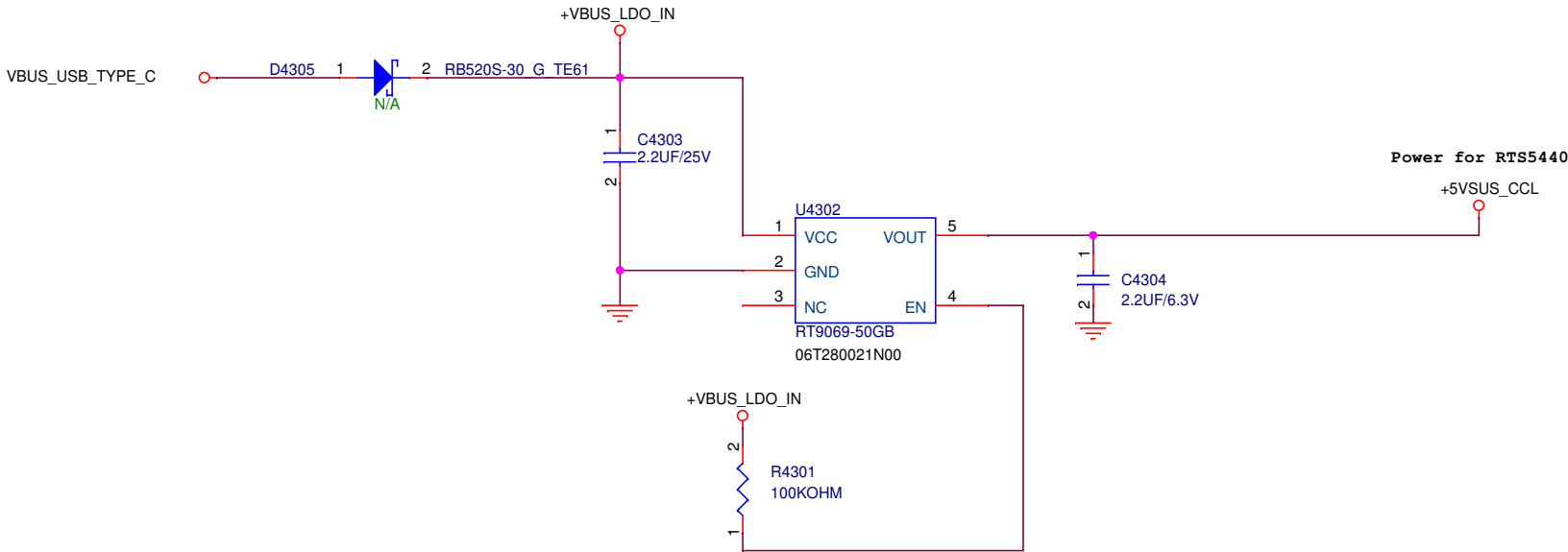
ESD

-Variant Name-

PEGATRON		Title	USB Type-C Receptacle
PEGATRON PROPRIETARY AND CONFIDENTIAL		Engineer:	Jay2_Liu
Size	Project Name	GU5FA	New
Custom			1.1
Date	Thursday, September 27, 2018	Sheet	45 of 54

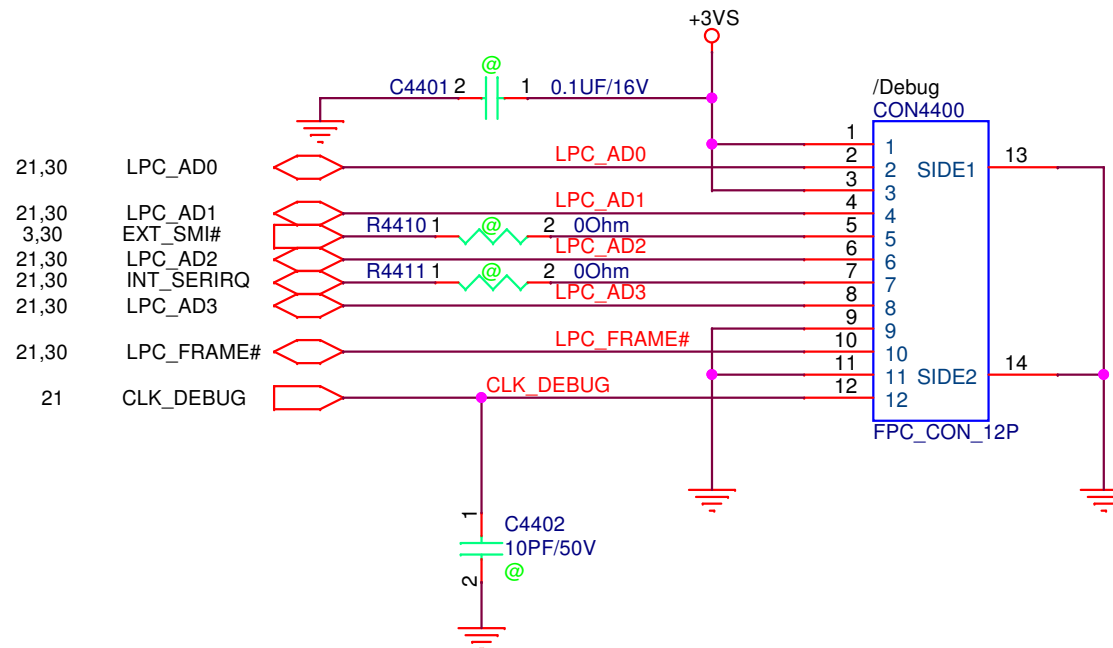
Hardware Solution For Dead Battery

VBUS_USB_TYPE_C		VBUS_USB_TYPE_C	41,42
+AC_BAT_SYS		+AC_BAT_SYS	41,45,80,81,82,83,88
+5VSUS_CCL		+5VSUS_CCL	41,42



<Variant Name>

<b>PEGATRON</b>		Title :		<b>Dead Battery</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL					
		Engineer:		<b>Jay2_Liu</b>	
Size <b>Custom</b>	Project Name <b>GU5FA</b>				Rev <b>1.1</b>
Date:		<b>Thursday, September 27, 2018</b>		Sheet	<b>43</b> of <b>94</b>



<Variant Name>

PEGATRON

Title : DEBUG CONN.

PEGATRON PROPRIETARY AND CONFIDENTIAL

BG1/HW1

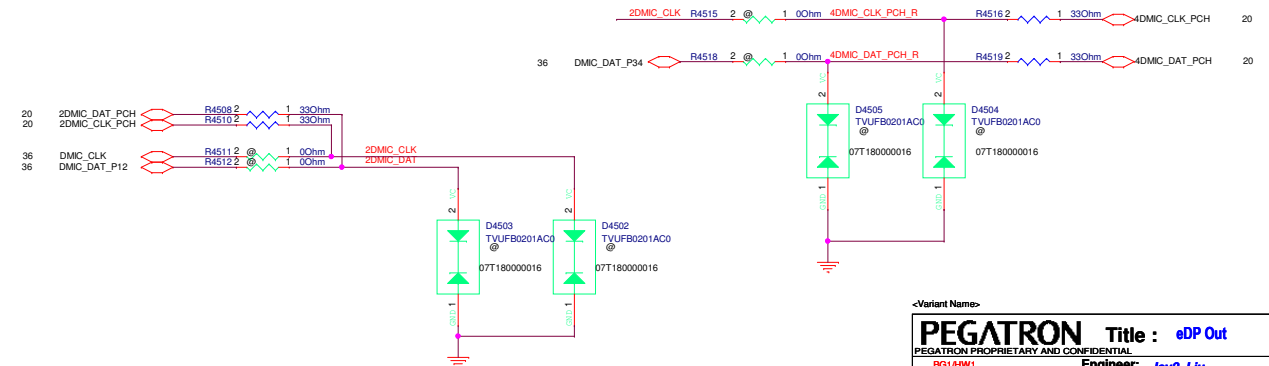
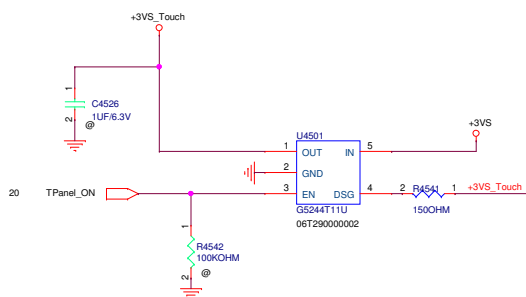
Engineer: Jay2\_Liu

Size	Project Name	Rev
Custom	GU5FA	1.1

Date: Thursday, September 27, 2018

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## Touch panel



	5	4	3	2	1
D					D
C					C
B					B
A					A

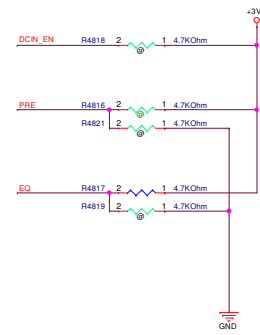
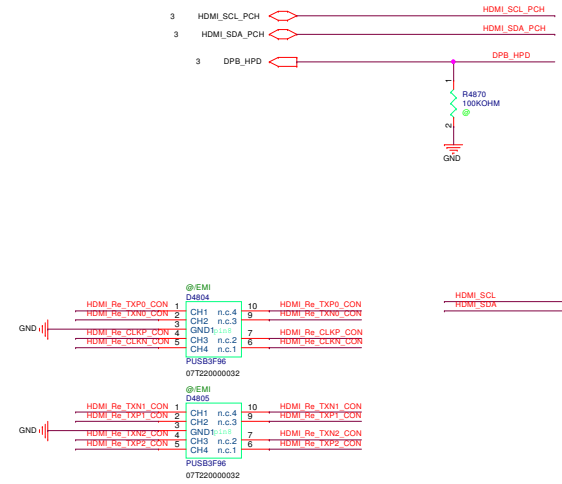
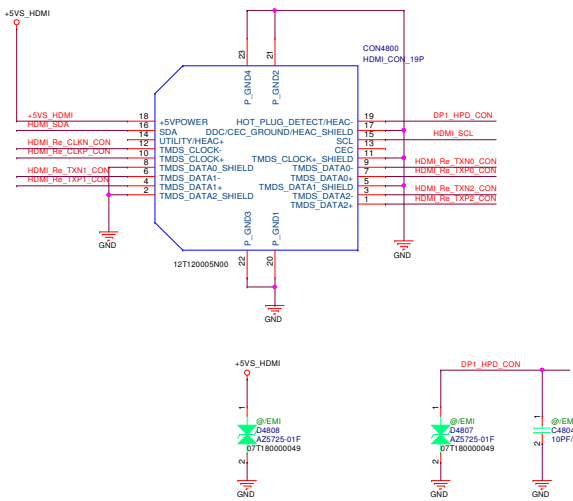
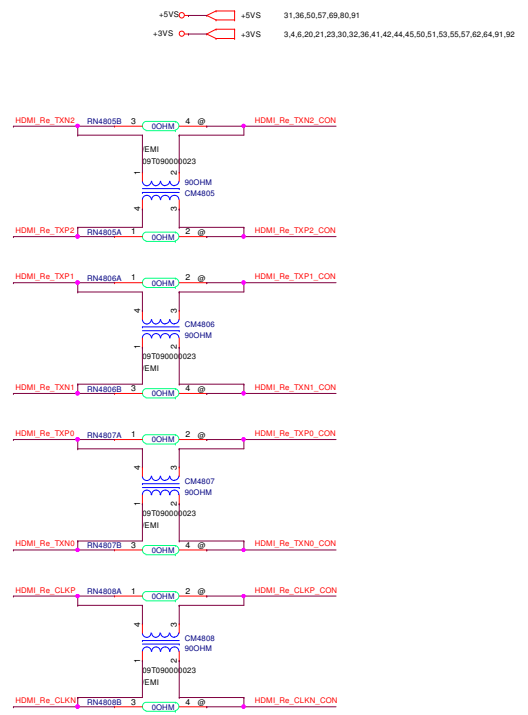
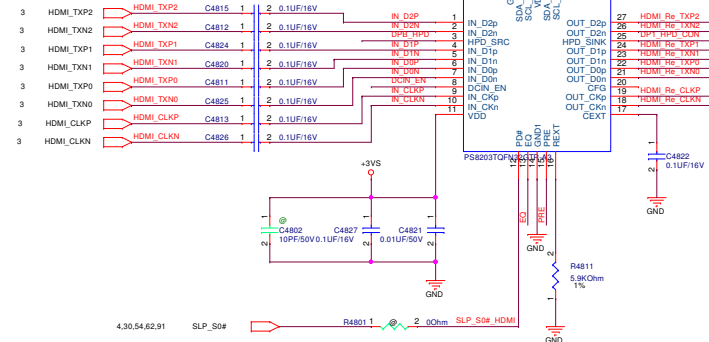
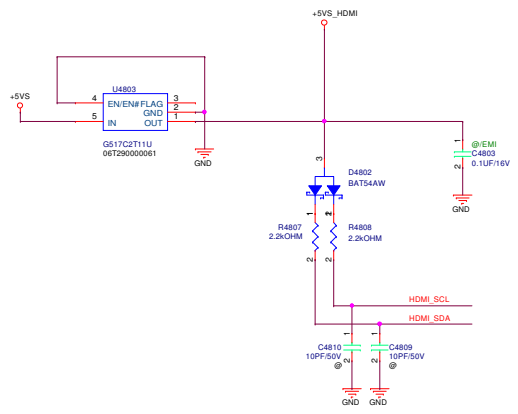
<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
46	Thursday, September 27, 2018	46	94

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
47	Thursday, September 27, 2018	47	94





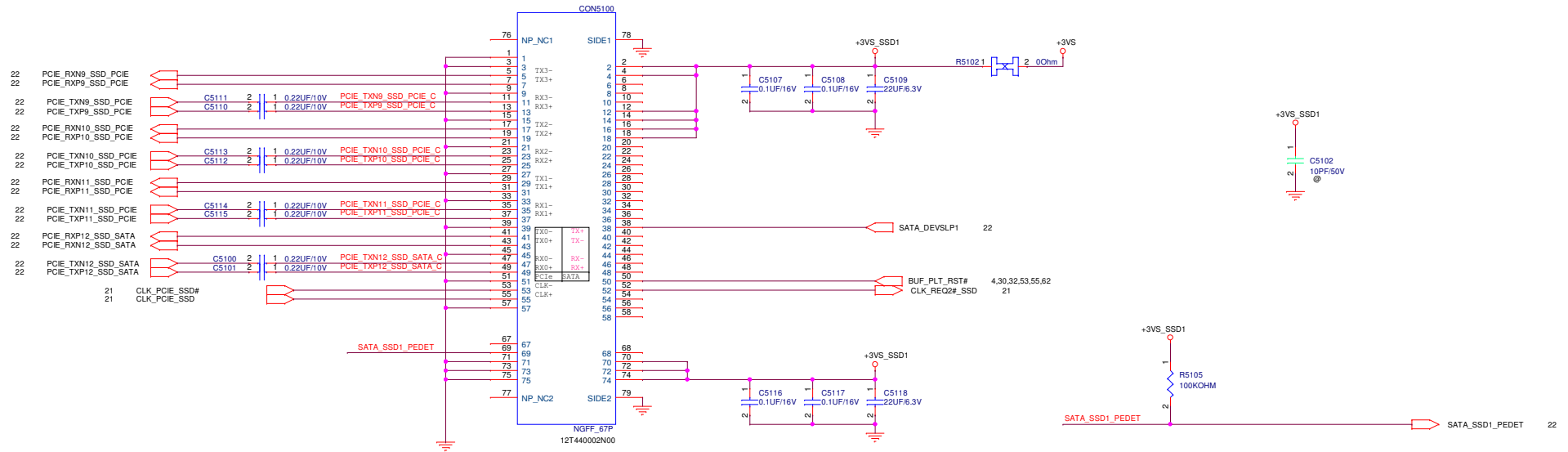


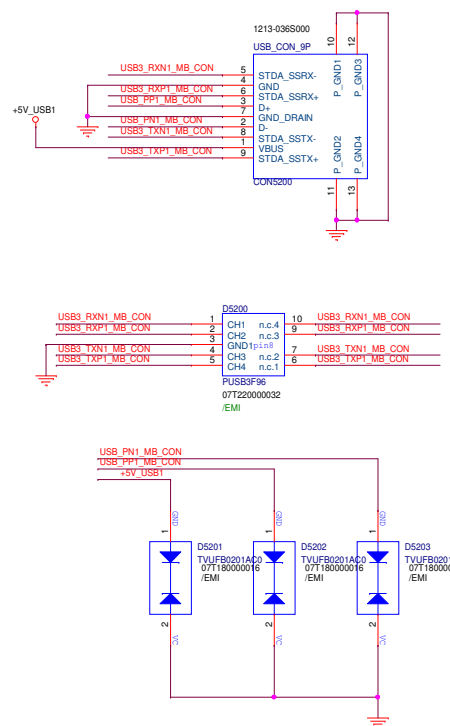
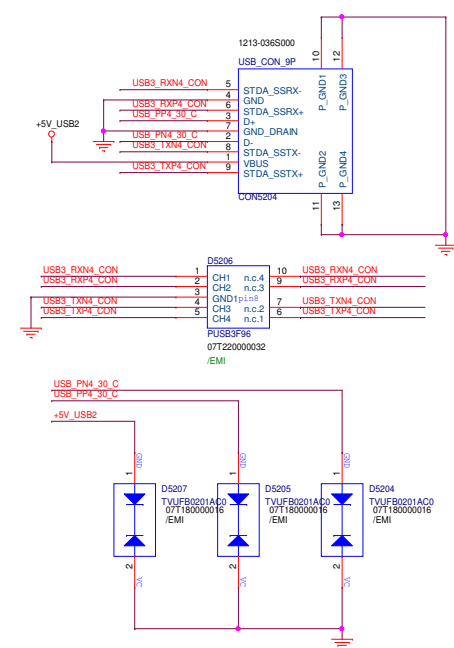
<Core Design>				
<div>PEGATRON</div> <div>PEGATRON PROPRIETARY AND CONFIDENTIAL</div> <div>Engineer: <u>Jay2 Liu</u></div>				
Size	Project Name			Rev
Custom	GU5FA			1
Date:	Thursday, September 27, 2018	49	94	
Sheet 1 of 94				



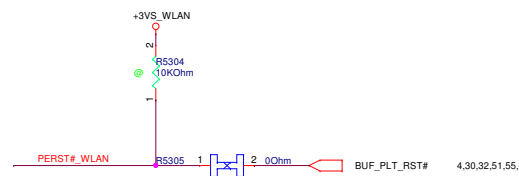
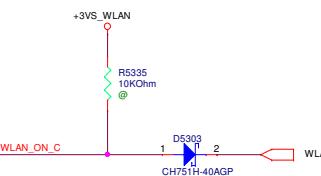
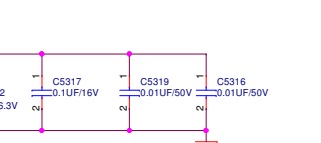
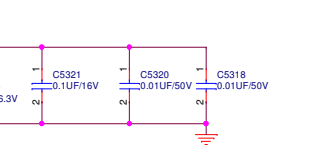
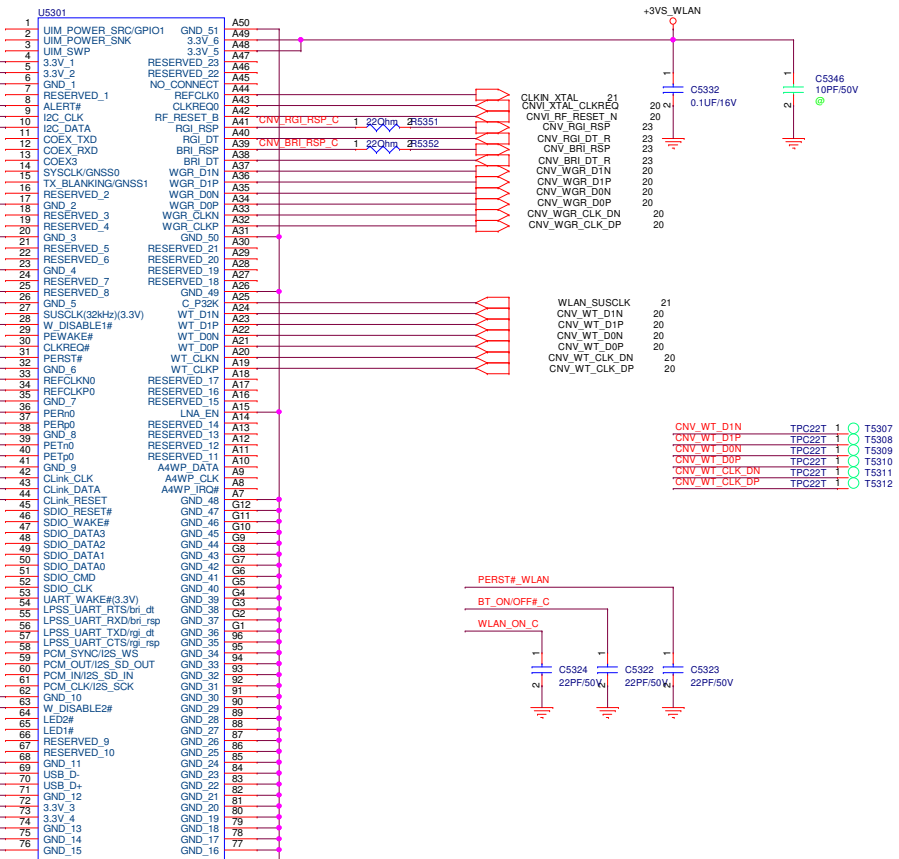
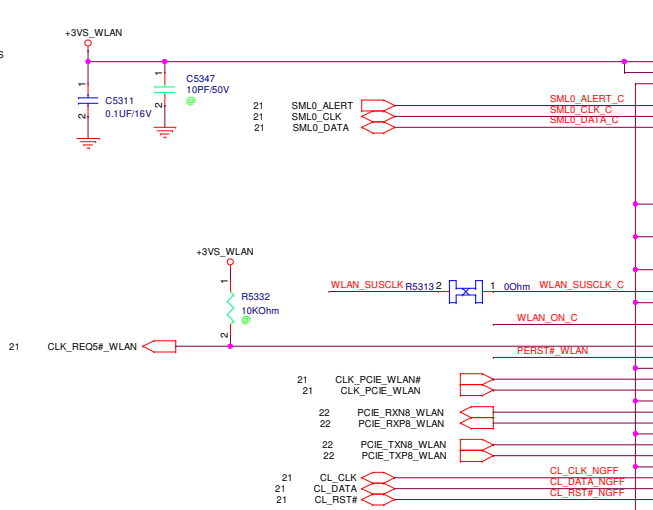
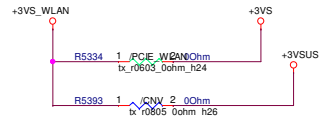
## SATA / PCIe SSD

SSD

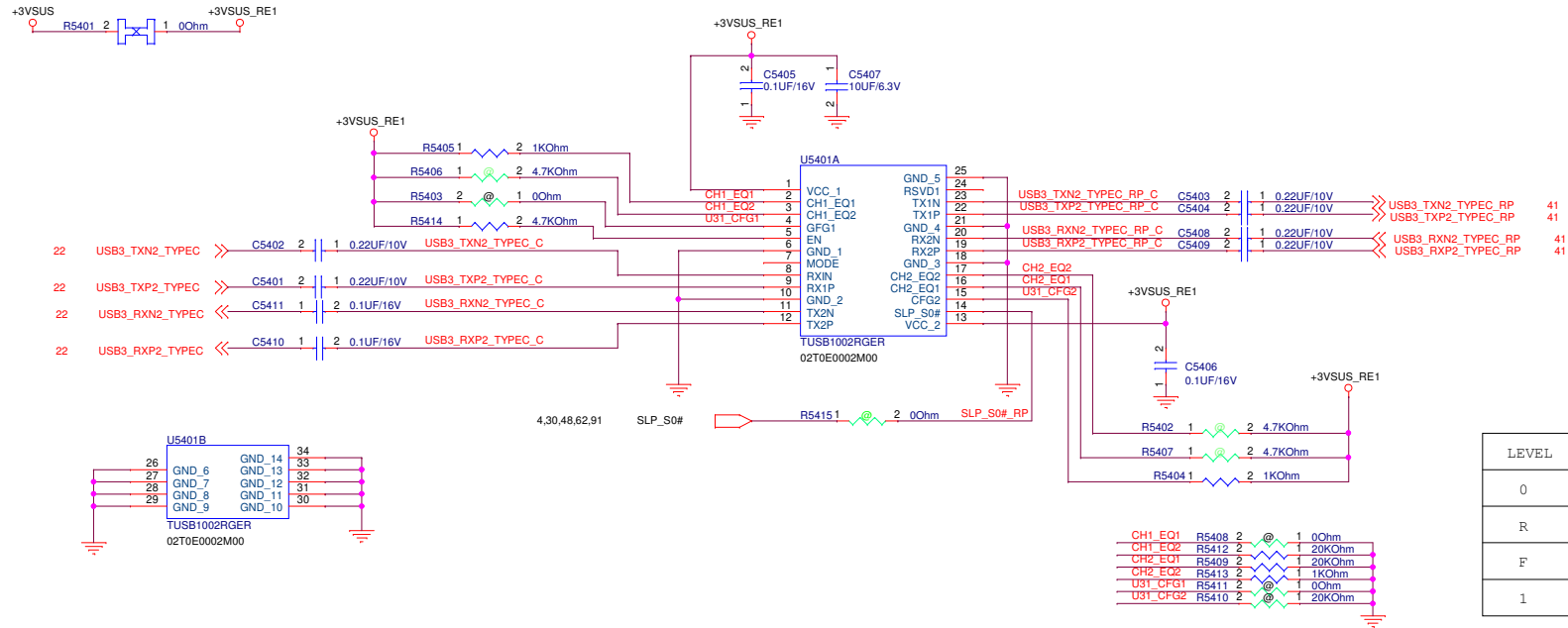




# WLAN



# USB3.0 re-driver (gen2)

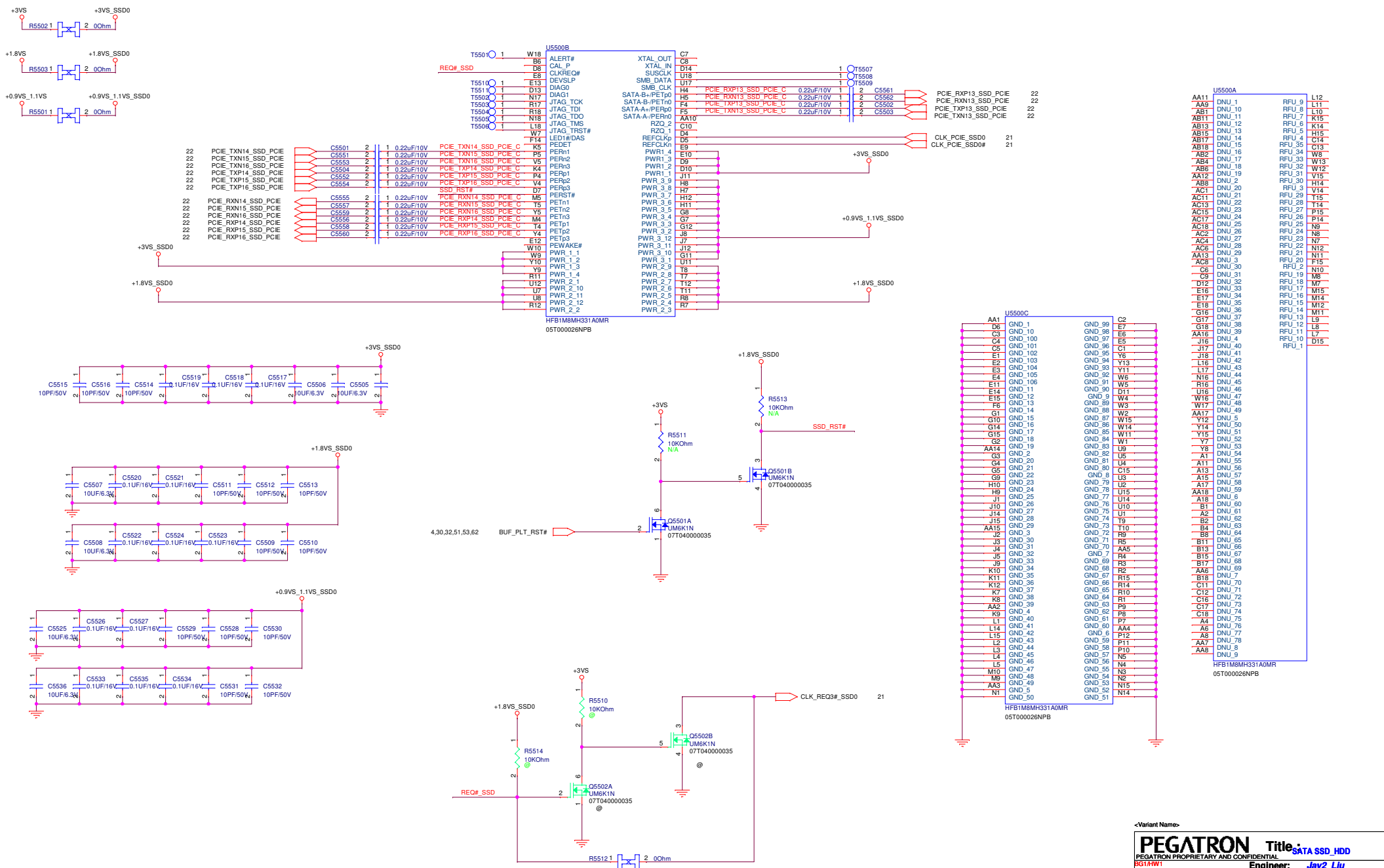


LEVEL	SETTINGS
0	Option 1: Tie 1 KΩ 5% to GND. Option 2: Tie directly to GND.
R	Tie 20 KΩ 5% to GND.
F	Float (leave pin open)
1	Option 1: Tie 1 KΩ 5% to VCC. Option 2: Tie directly to VCC.

EQ SETTING #	CHx_EQ2 PIN LEVEL	CHx_EQ1 PIN LEVEL	EQ GAIN at 2.5GHz / 5 GHz (dB)
1	0	0	1.9 / 5.5
2	0	R	2.8 / 7.1
3	0	F	3.5 / 8.2
4	0	1	4.4 / 9.3
5	R	0	5.0 / 10.2
6	R	R	5.8 / 11.1
7	R	F	6.4 / 11.8
8	R	1	7.1 / 12.6
9	F	0	7.6 / 13.1
10	F	R	8.2 / 13.8
11	F	F	8.7 / 14.3
12	F	1	9.2 / 14.8
13	1	0	9.6 / 15.2
14	1	R	10.1 / 15.6
15	1	F	Reserved.
16	1	1	Reserved.

SETTING #	CFG1 PIN LEVEL	CFG2 PIN LEVEL	CH1 DC GAIN (dB)	CH2 DC GAIN (dB)	CH1 VOD LINEAR RANGE (mVpp)	CH2 VOD LINEAR RANGE (mVpp)
1	0	0	+1	0	900	900
2	0	R	0	+1	900	900
3	0	F	0	0	900	900
4	0	1	+1	+1	900	900
5	R	0	0	0	1000	1000
6	R	R	+1	0	1000	1000
7	R	F	0	-1	1000	1000
8	R	1	+2	+2	1000	1000
9	F	0	-1	-1	1200	1200
10	F	R	-2	-2	1200	1200
11	F	F	0	0	1200	1200
12	F	1	+1	+1	1200	1200
13	1	0	-1	0	1200	1200
14	1	R	0	-1	1200	1200
15	1	F	0	+1	1200	1200
16	1	1	+1	0	1200	1200

## PCIe SSD

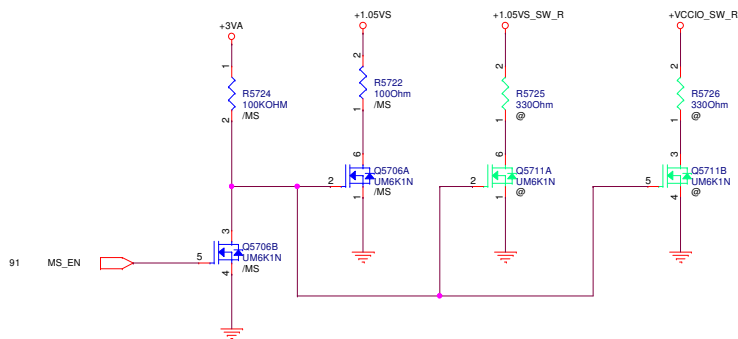
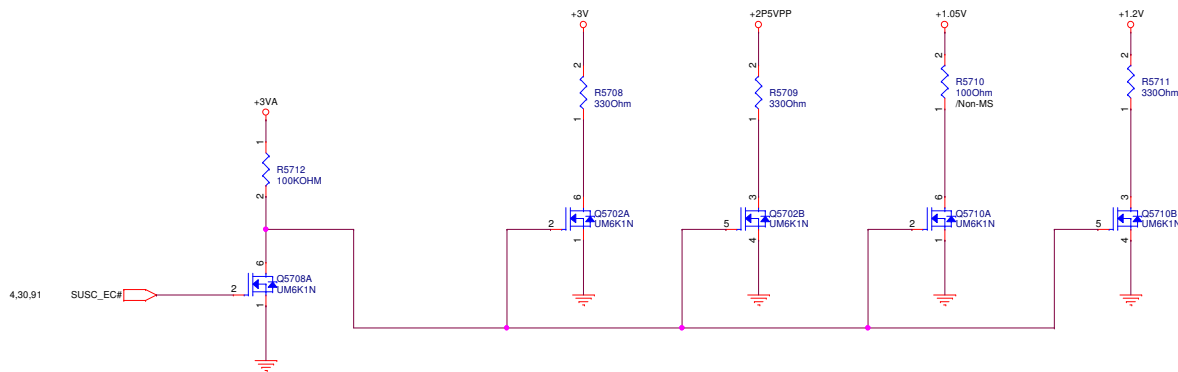
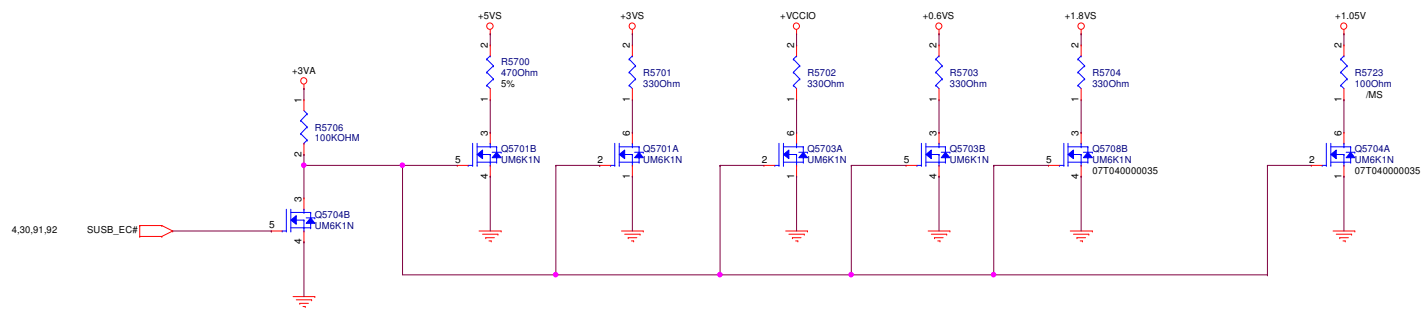






+3VA		+3VA	21,30,31,36,64,81,88,93
+5VS		+5VS	31,36,48,50,69,80,91
+3VS		+3VS	3,4,6,20,21,23,30,32,36,41,42,44,45,48,50,51,53,55,62,64,91,92
+VCCIO		+VCCIO	3,9,91
+0.6VS		+0.6VS	15,83
+1.8VS		+1.8VS	20,36,45,55,91

+3V		+3V	31,41,91
+1.05V		+1.05V	9,32,91
+2P5VPP		+2P5VPP	16,17,82
+1.2V		+1.2V	6,9,15,16,17,19,83



5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

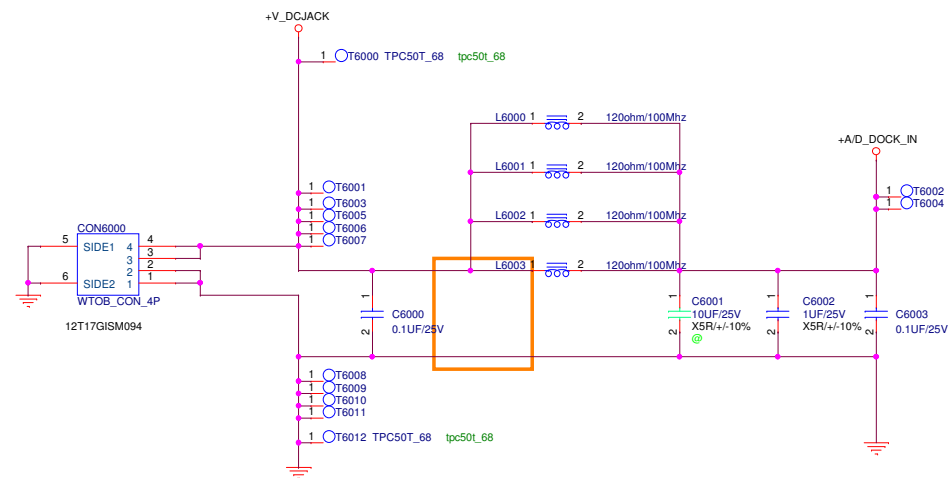
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
58	Thursday, September 27, 2018	58	94

5	4	3	2	1
D				D
C				C
B				B
A				A

<Variant Name>

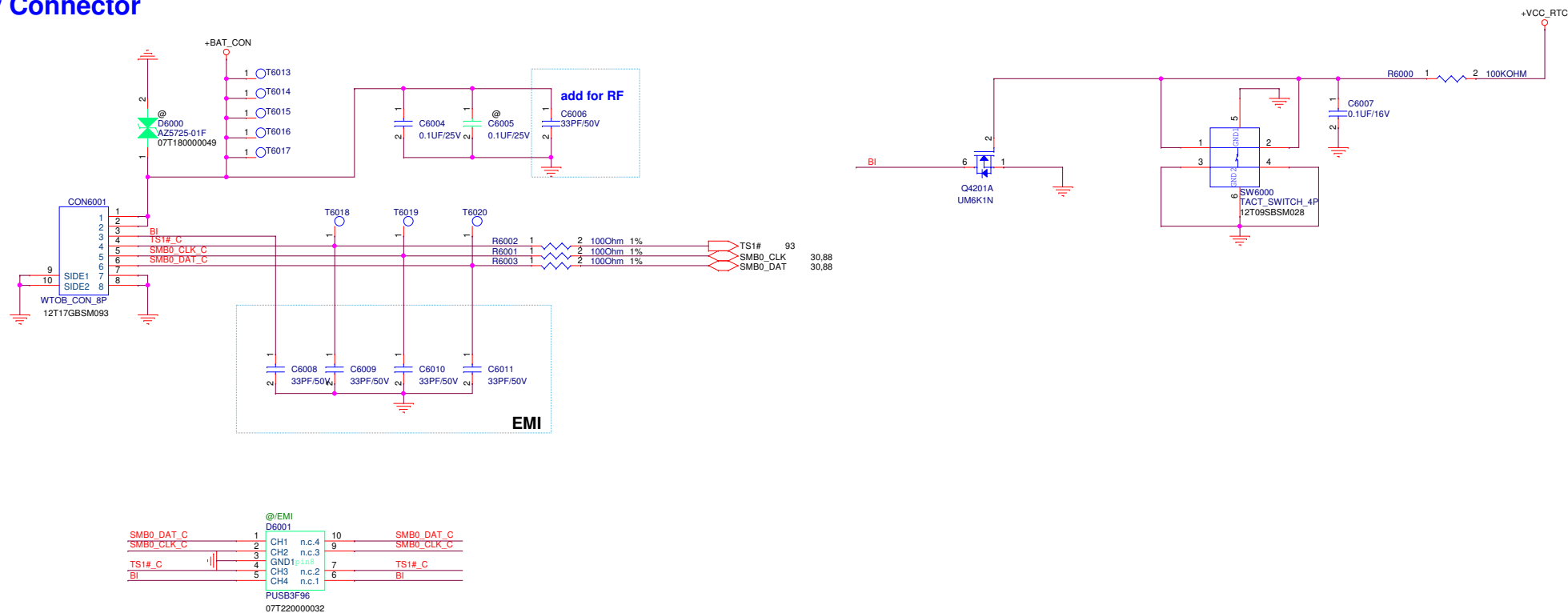
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
59	Thursday, September 27, 2018	59	94

**DC Jack WTB CONN**



+VCC_RTC			+VCC_RTC	4,10,21
+A/D_DOCK_IN			+A/D_DOCK_IN	89
+BAT_CON			+BAT_CON	88

## Battery Connector



9

1

C

1

B

1

A

1

<Variant Name>

PEGATRON Title <Title>

PEGATRON PROPRIETARY AND CONFIDENTIAL

BGI/HWT

**Engineer:**

**.lav2 Liu**

Size	Project Name
------	--------------

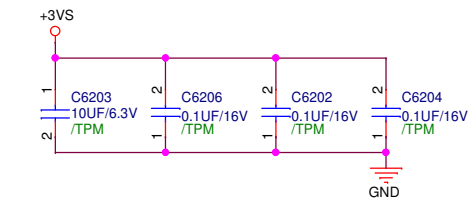
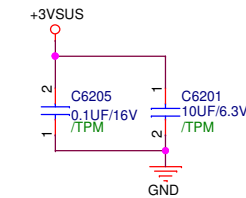
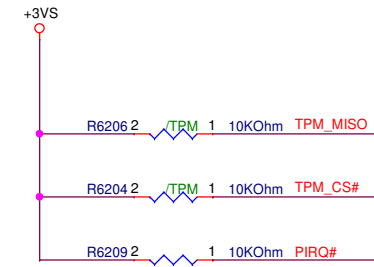
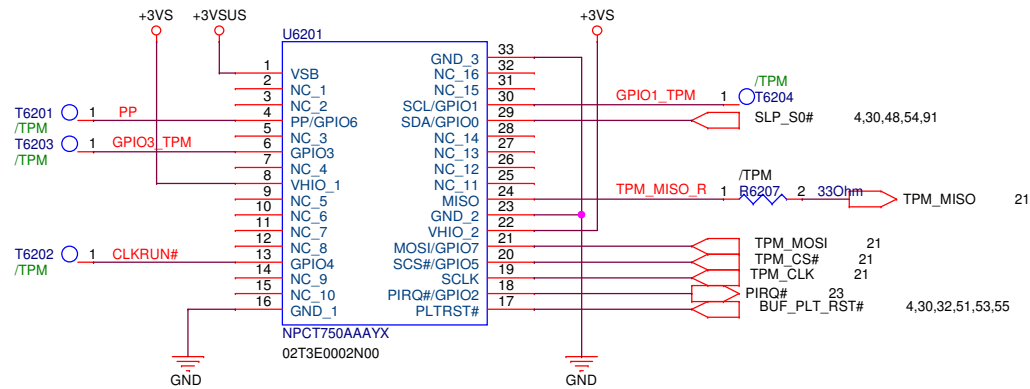
Thursday, September 27, 2018

1.1

61

61  
Shoot

# 62 TPM NPCT650/750 - SPI

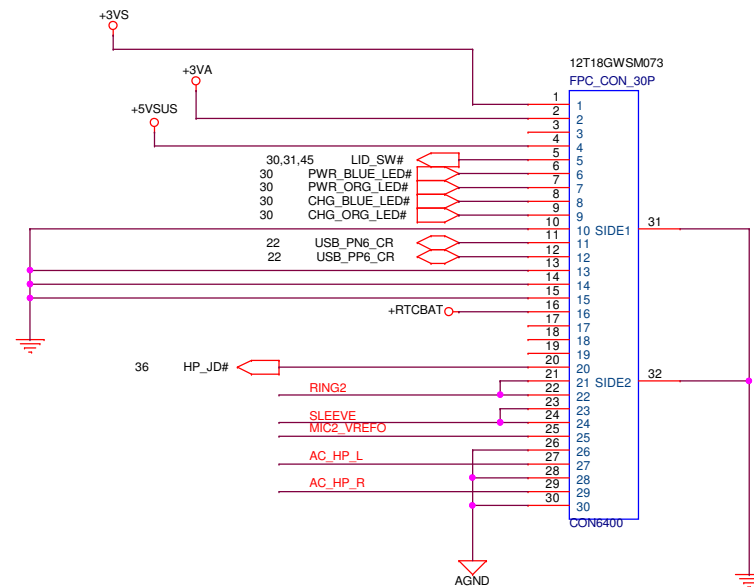
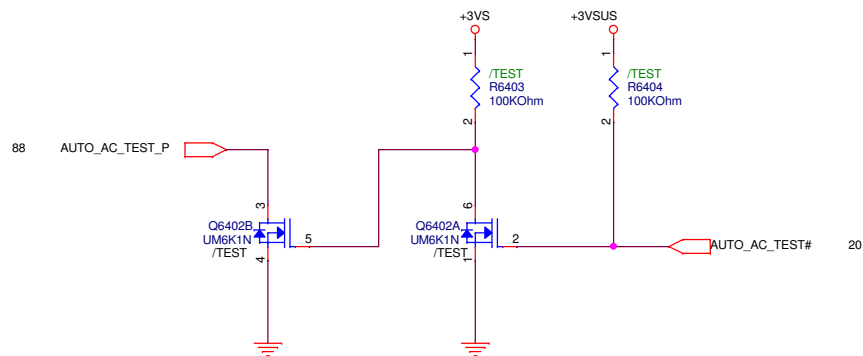
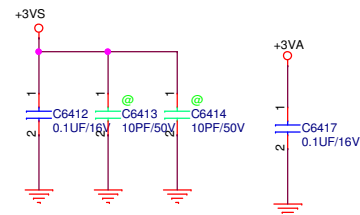
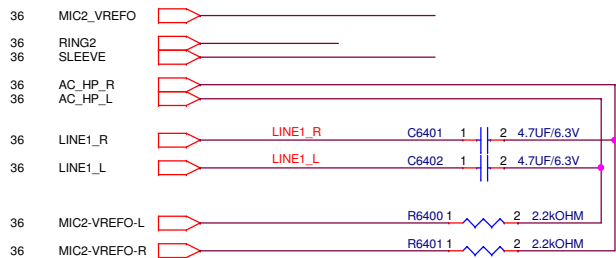


<Variant Name>

<b>PEGATRON</b>		Title :TPM NPCT650	
REGATRON PROPRIETARY AND CONFIDENTIAL		Jay2_Liu	
Engineer:			
Size Custom	Project Name CU5FA	Rev 1.1	
Date: Thursday, September 27, 2018		Sheet 62 of 94	



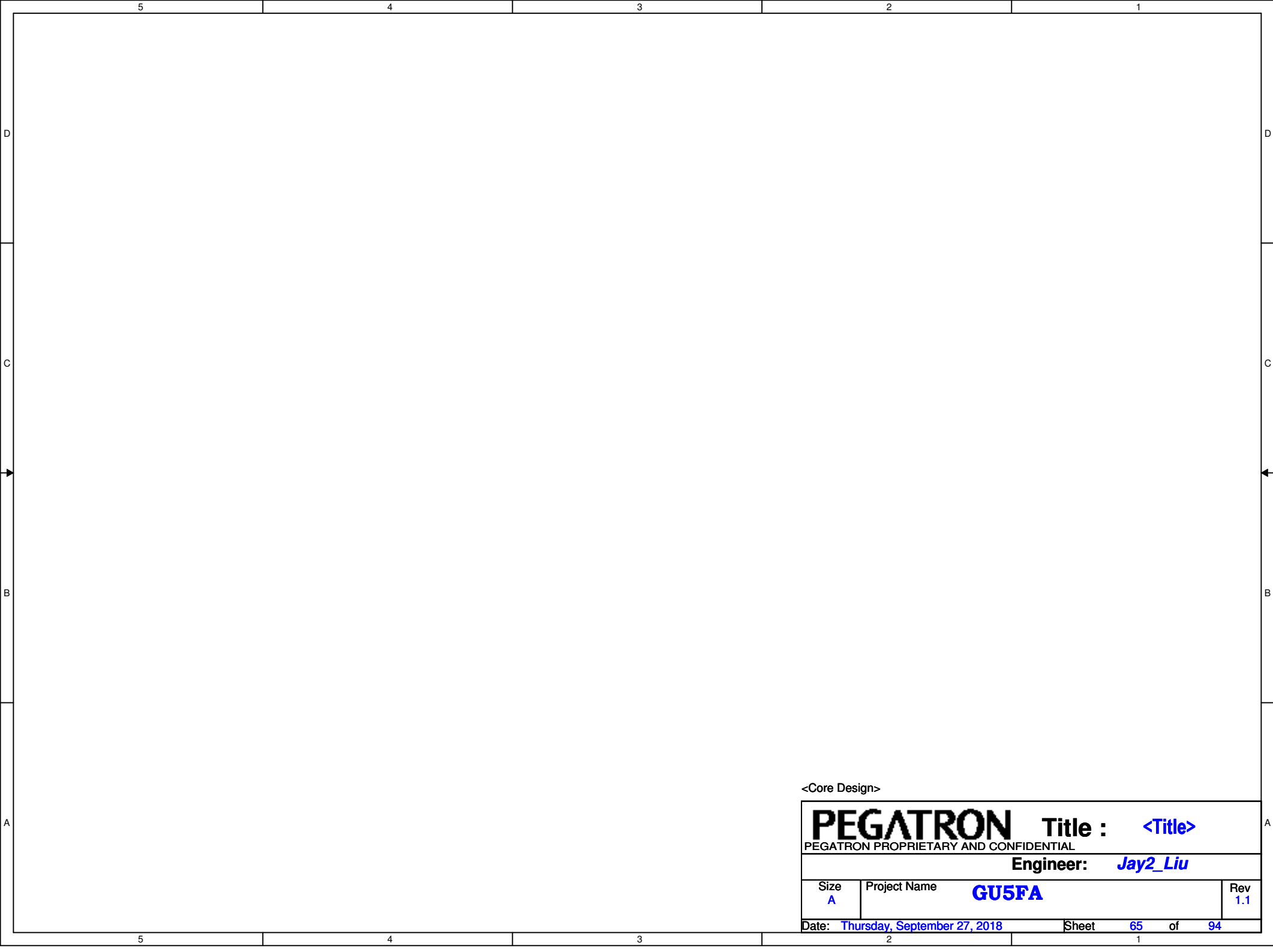
<Core Design>			
<b>PEGATRON</b>		Title :<Title>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
		Engineer: Jay2_Liu	
Size B	Project Name GU5FA		Rev 1.1
Date: Thursday, September 27, 2018		Sheet 63	of 94



<Variant Name>

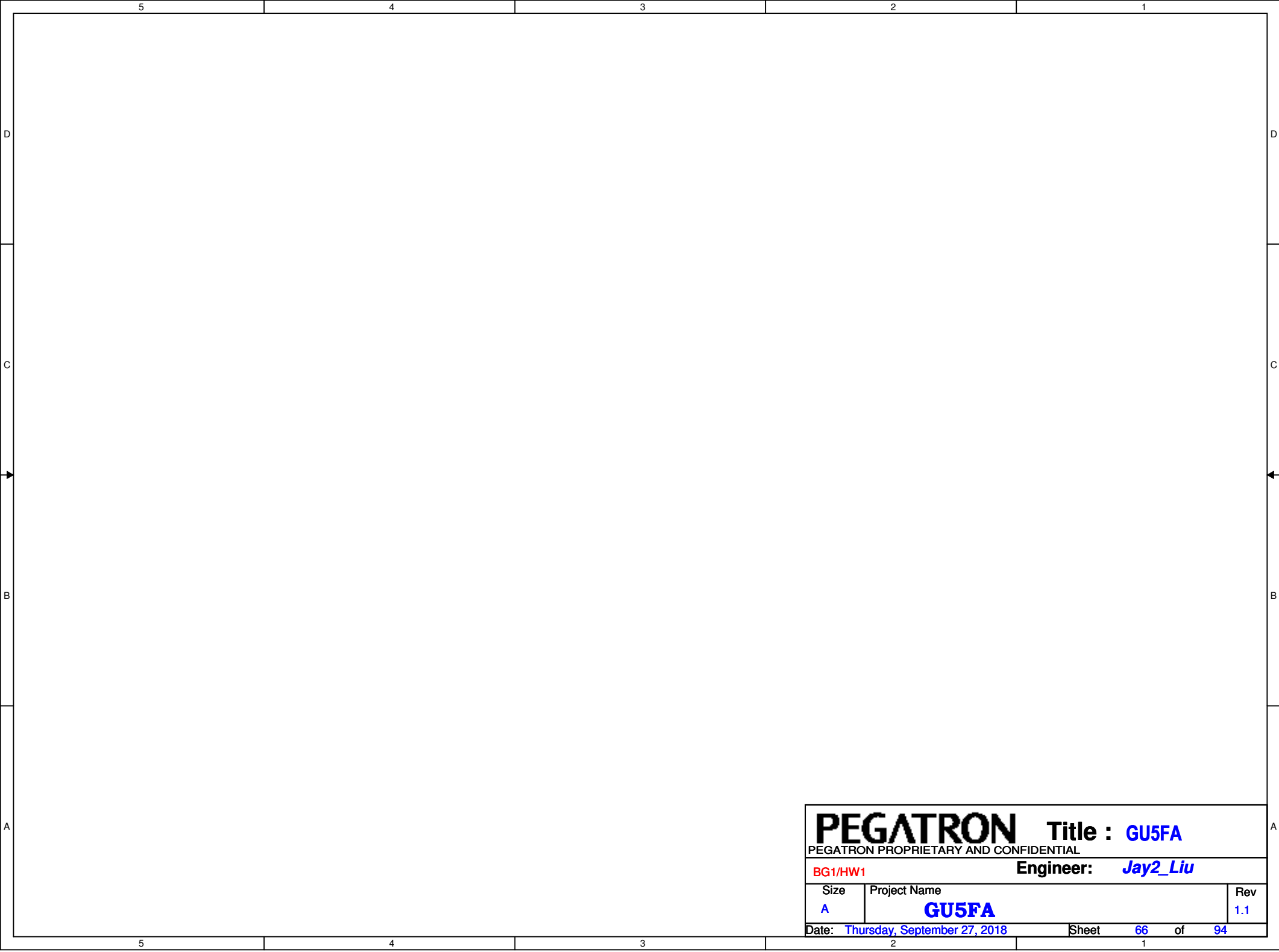
<b>PEGATRON</b>		Title :IO Board	
PEGATRON PROPRIETARY AND CONFIDENTIAL		Jay2_Liu	
Engineer:			
Size	Project Name	Rev	
Custom	GU5FA	1.1	
Date:	Thursday, September 27, 2018	Sheet	64 of 94





<Core Design>

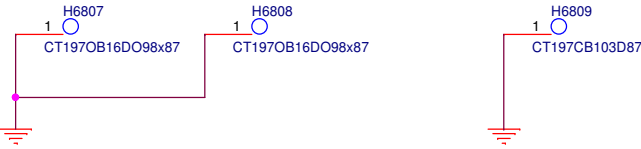
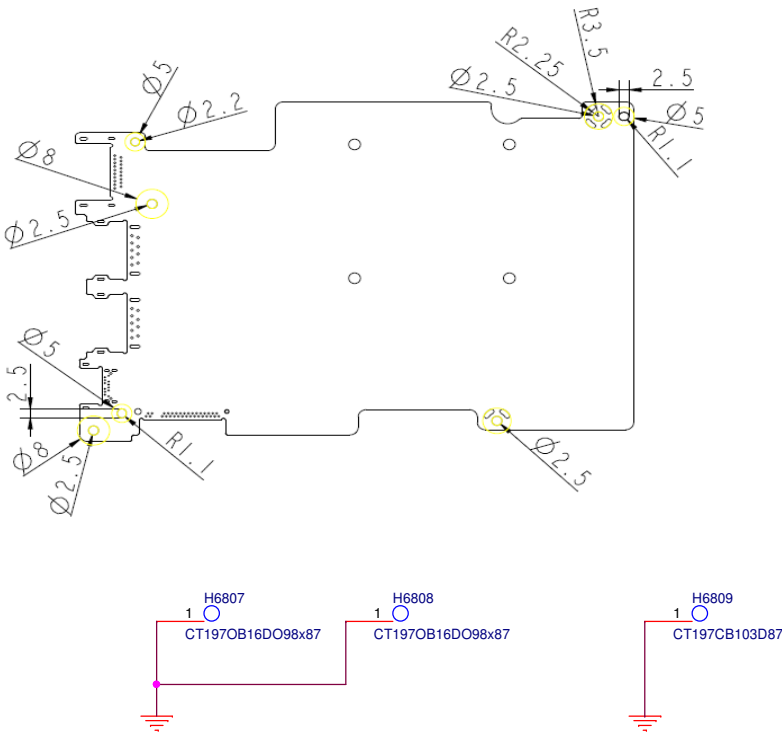
<b>PEGATRON</b>		<b>Title :</b>	<b>&lt;Title&gt;</b>
PEGATRON PROPRIETARY AND CONFIDENTIAL			
		<b>Engineer:</b>	<b><i>Jay2_Liu</i></b>
Size <b>A</b>	Project Name <b>GU5FA</b>		Rev <b>1.1</b>
Date: <b>Thursday, September 27, 2018</b>		Sheet	<b>65</b> of <b>94</b>



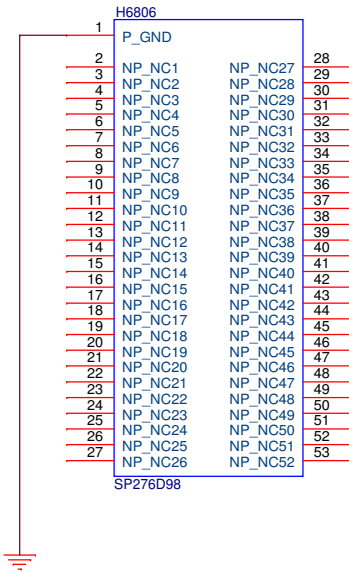
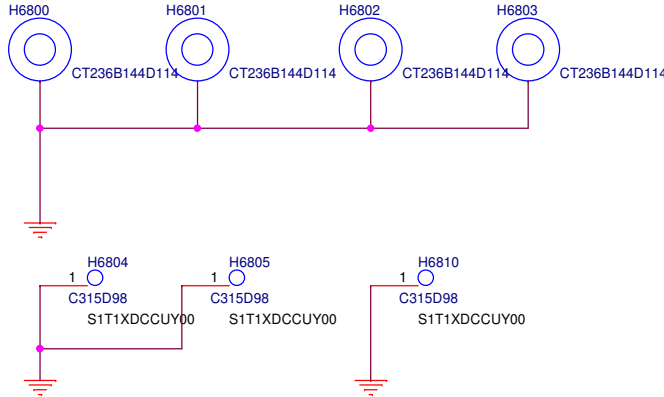
<b>PEGATRON</b>		<b>Title :</b> <b>GU5FA</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>BG1/HW1</b>		<b>Engineer:</b> <b><i>Jay2_Liu</i></b>	
Size <b>A</b>	Project Name <b>GU5FA</b>		Rev <b>1.1</b>
Date: <b>Thursday, September 27, 2018</b>		Sheet <b>66</b> of <b>94</b>	

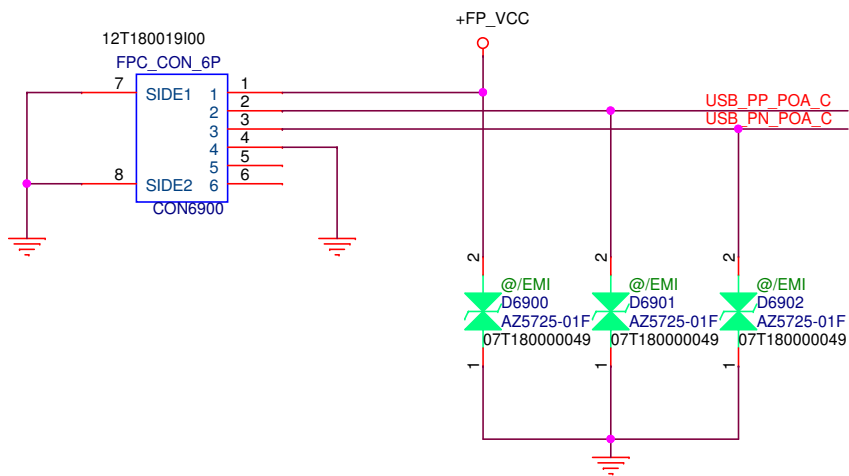
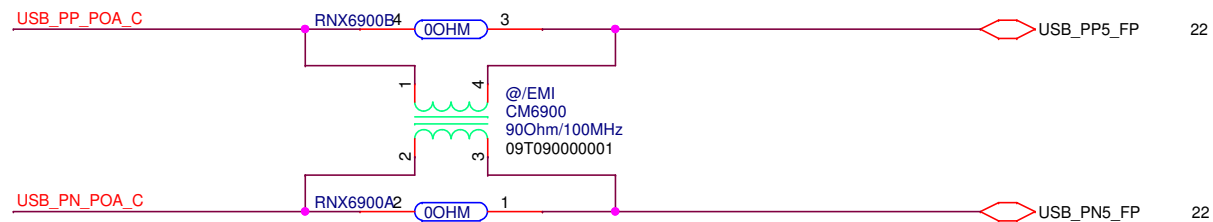
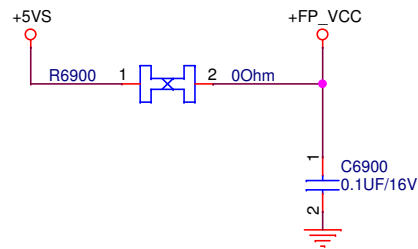


68 ME Hole



CPU NUT

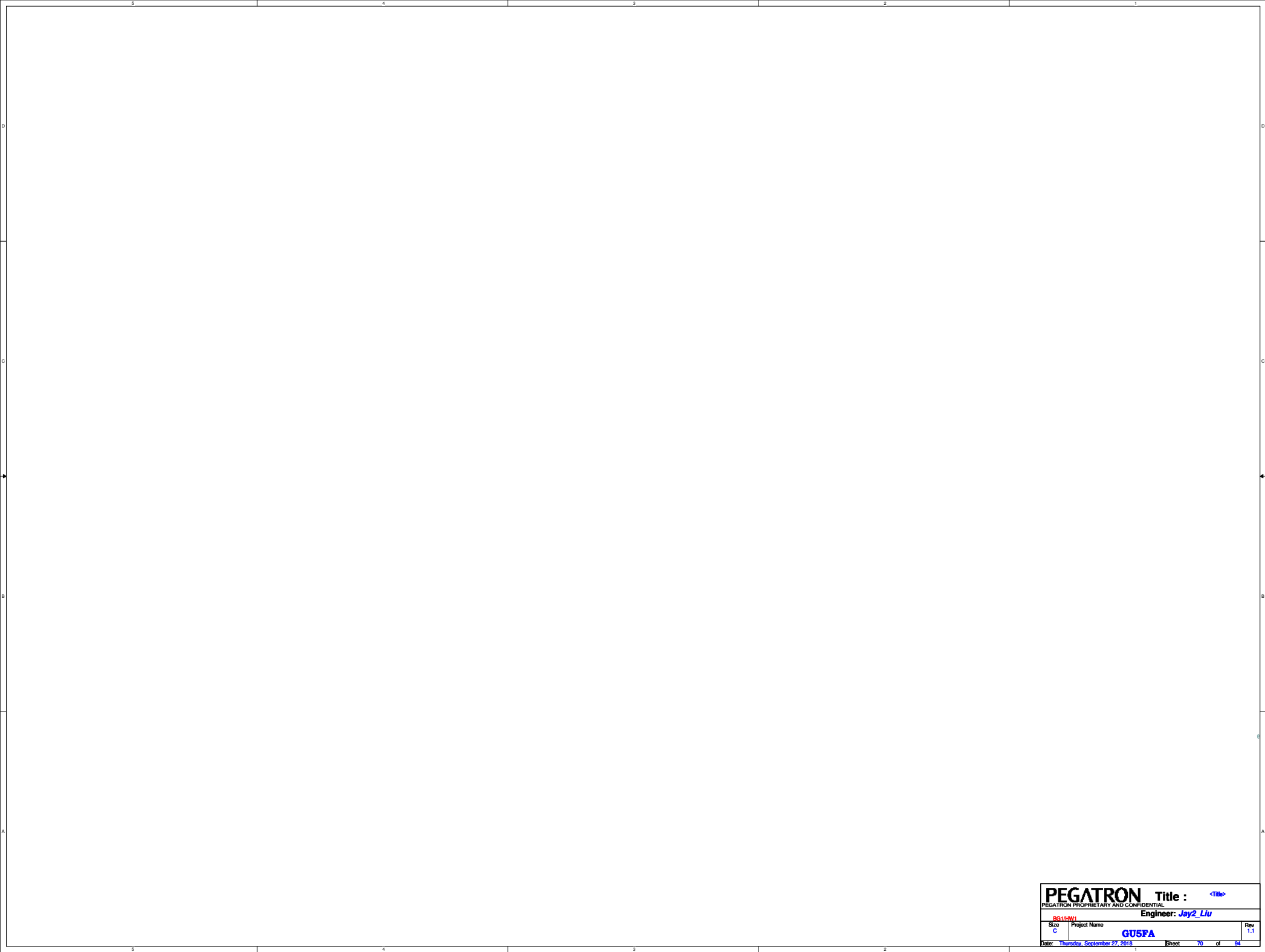




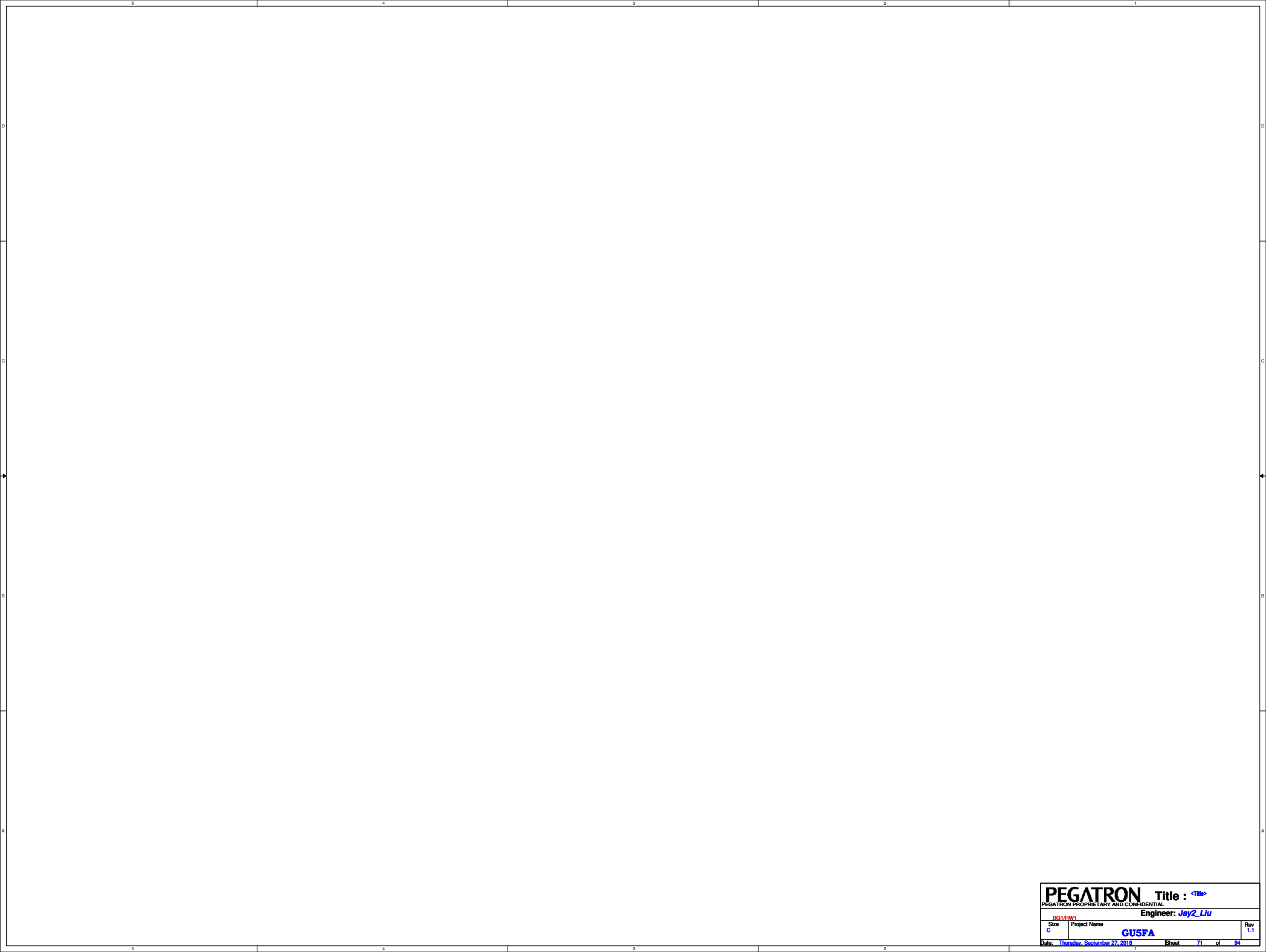
Pin	Definition
1	+FP_VCC
2	USBP
3	USBN
4	GND
5	NC
6	NC

<Variant Name>

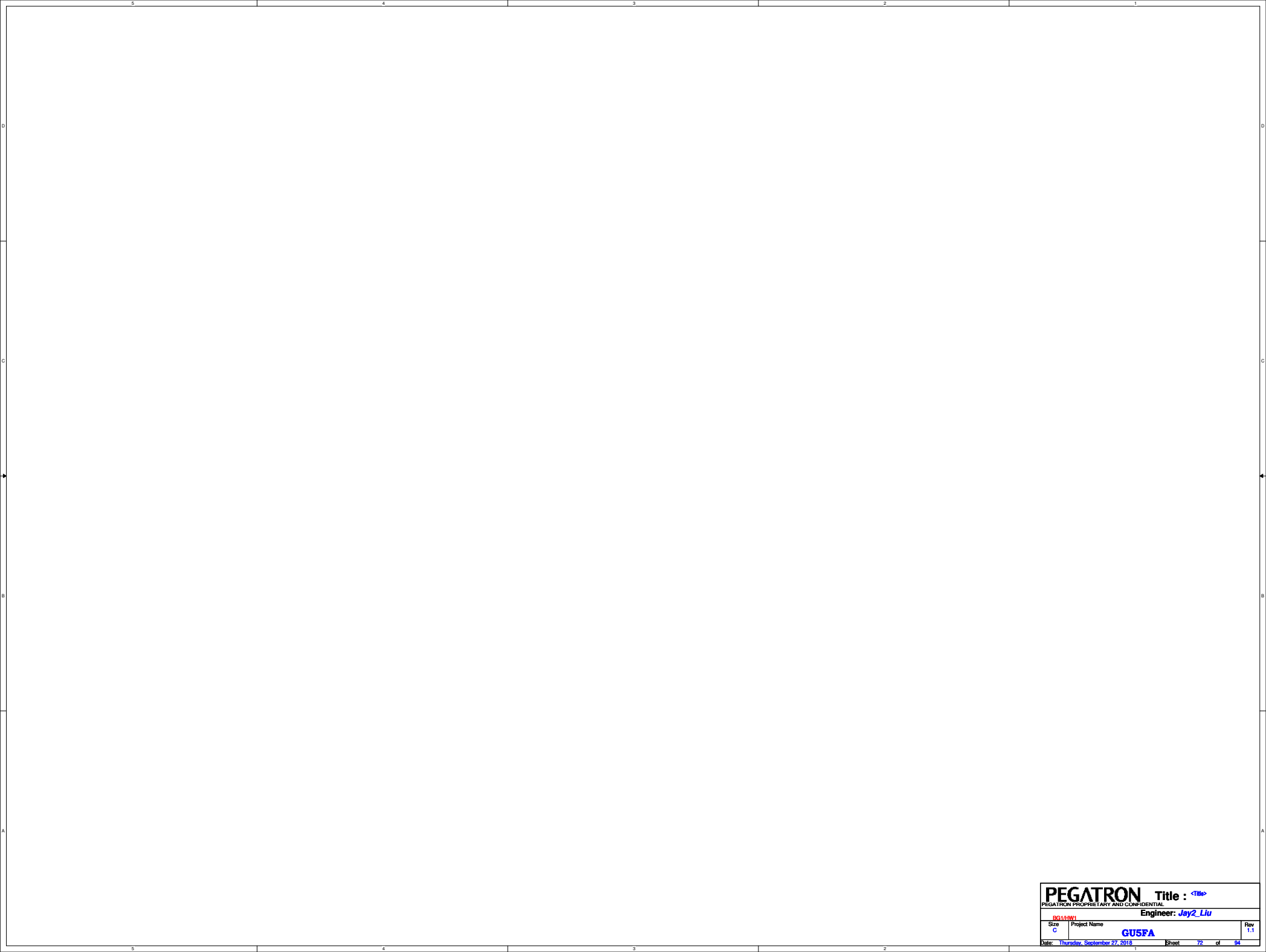
<b>PEGATRON</b>		Title : <b>Finger printer</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
BG1/HW1		Engineer: <b>Jay2_Liu</b>	
Size Custom	Project Name <b>GU5FA</b>		Rev 1.1
Date: <b>Thursday, September 27, 2018</b>		Sheet <b>69</b> of <b>94</b>	



<b>PEGATRON</b>		<b>Title :</b>	<Title>
PEGATRON PROPRIETARY AND CONFIDENTIAL			
BG1/DW1		<b>Engineer:</b> Jay2_Liu	
Size C	Project Name <b>GU5FA</b>		Rev 1.1
Date: Thursday, September 27, 2018		Sheet	70 of 84



<b>PEGATRON</b>		<b>Title :</b> <Title>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
BG1/DW1		Engineer: Jay2_Liu	
Size C	Project Name <b>GU5FA</b>		Rev 1.1
Date: Thursday, September 27, 2018		Sheet	71 of 84



<b>PEGATRON</b>		<b>Title :</b> <Title>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
BG1/1W1		Engineer: Jay2_Liu	
Size C	Project Name  <b>GU5FA</b>	Rev 1.1	
Date: Thursday, September 27, 2018		Sheet	72 of 94





<b>PEGATRON</b>		<b>Title :</b> <Title>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
BG11HW1		Engineer: Jay2_Liu	
Size C	Project Name <b>GUSFA</b>		Rev 1.1
Date: Thursday, September 27, 2018		Sheet	79 of 84









<b>PEGATRON</b>		<b>Title :</b> <Title>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
BG11HW1		Engineer: <i>Jay2_Liu</i>	
Size C	Project Name <b>GU5FA</b>		Rev 1.1
Date: <i>Thursday, September 27, 2018</i>		Sheet	77 of 84

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

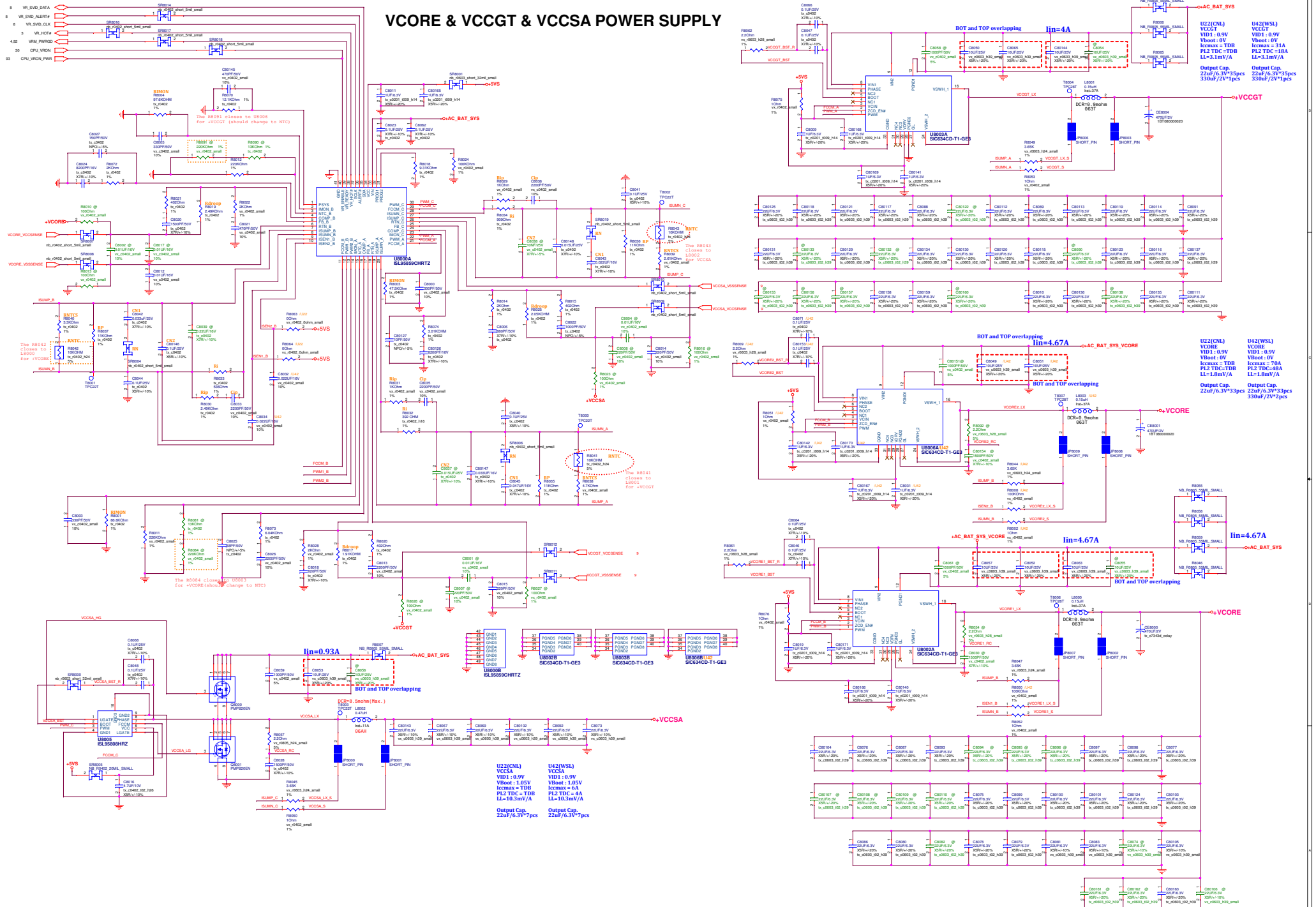
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
78	Thursday, September 27, 2018	94	

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

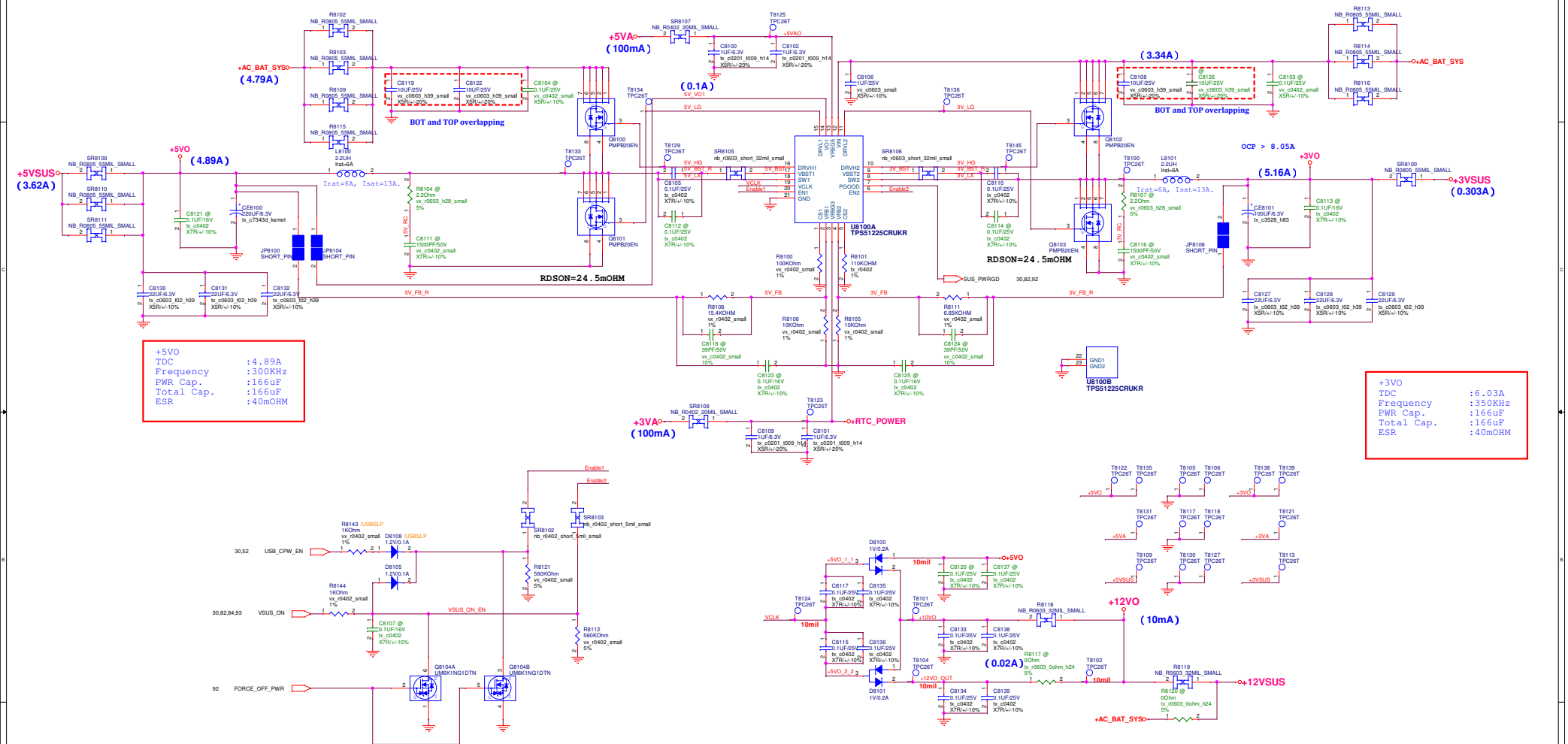
<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
Thursday, September 27, 2018	79	94	

## VCORE & VCCGT & VCCSA POWER SUPPLY

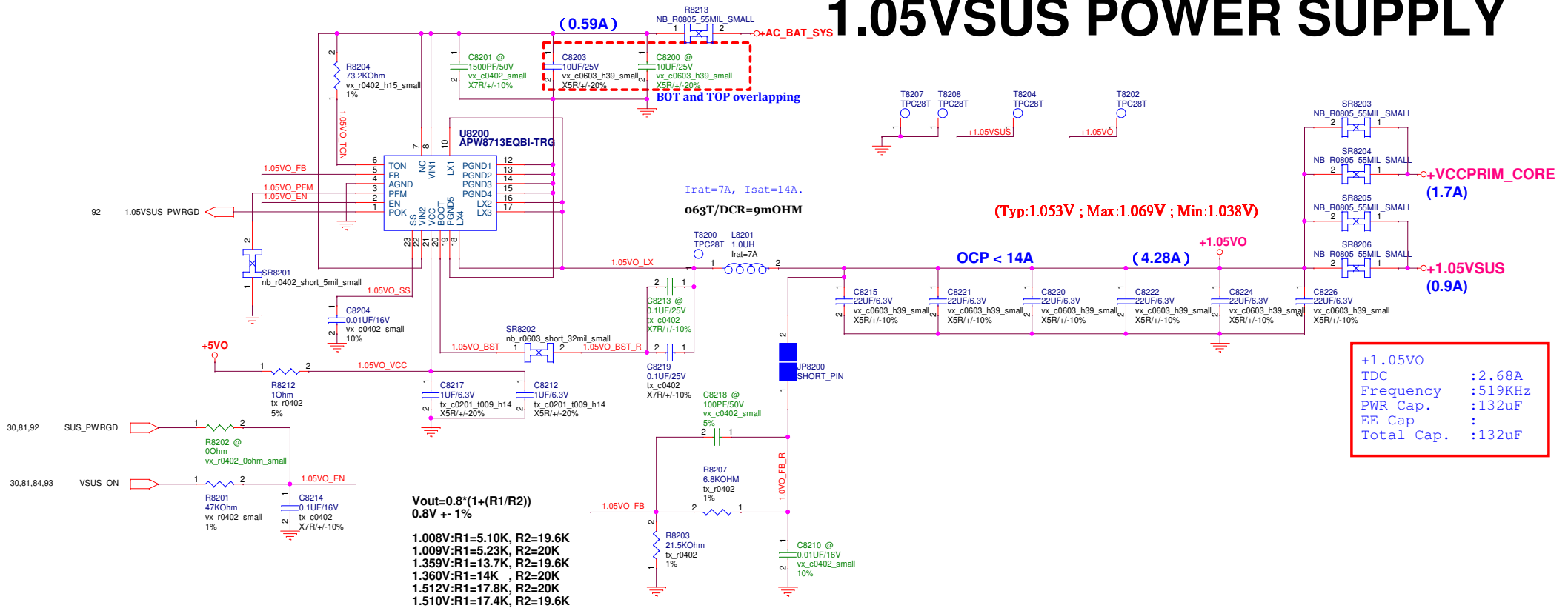




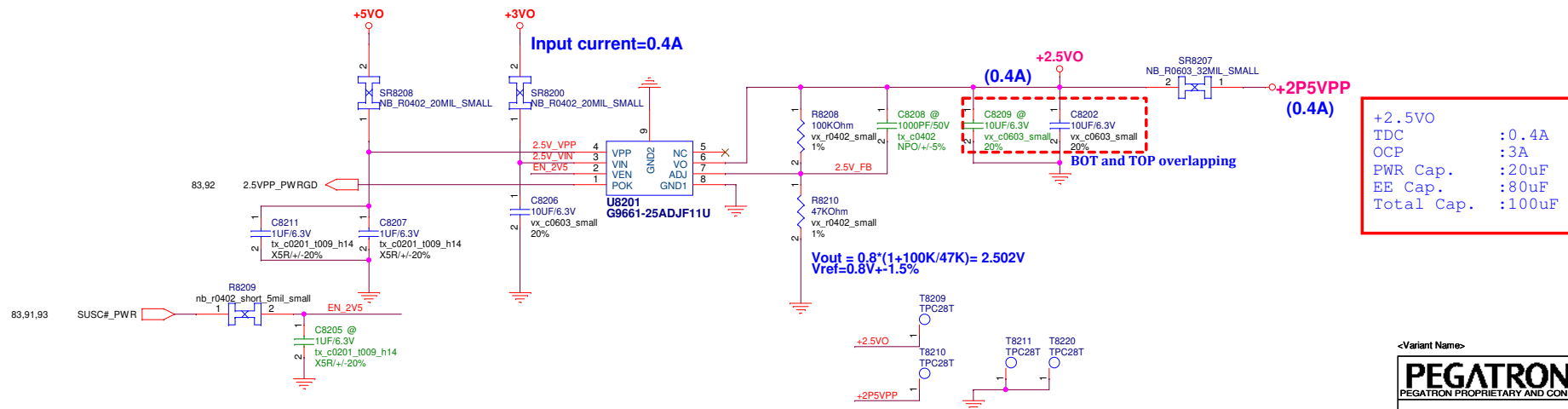
## 5VO & 3VO POWER SUPPLY



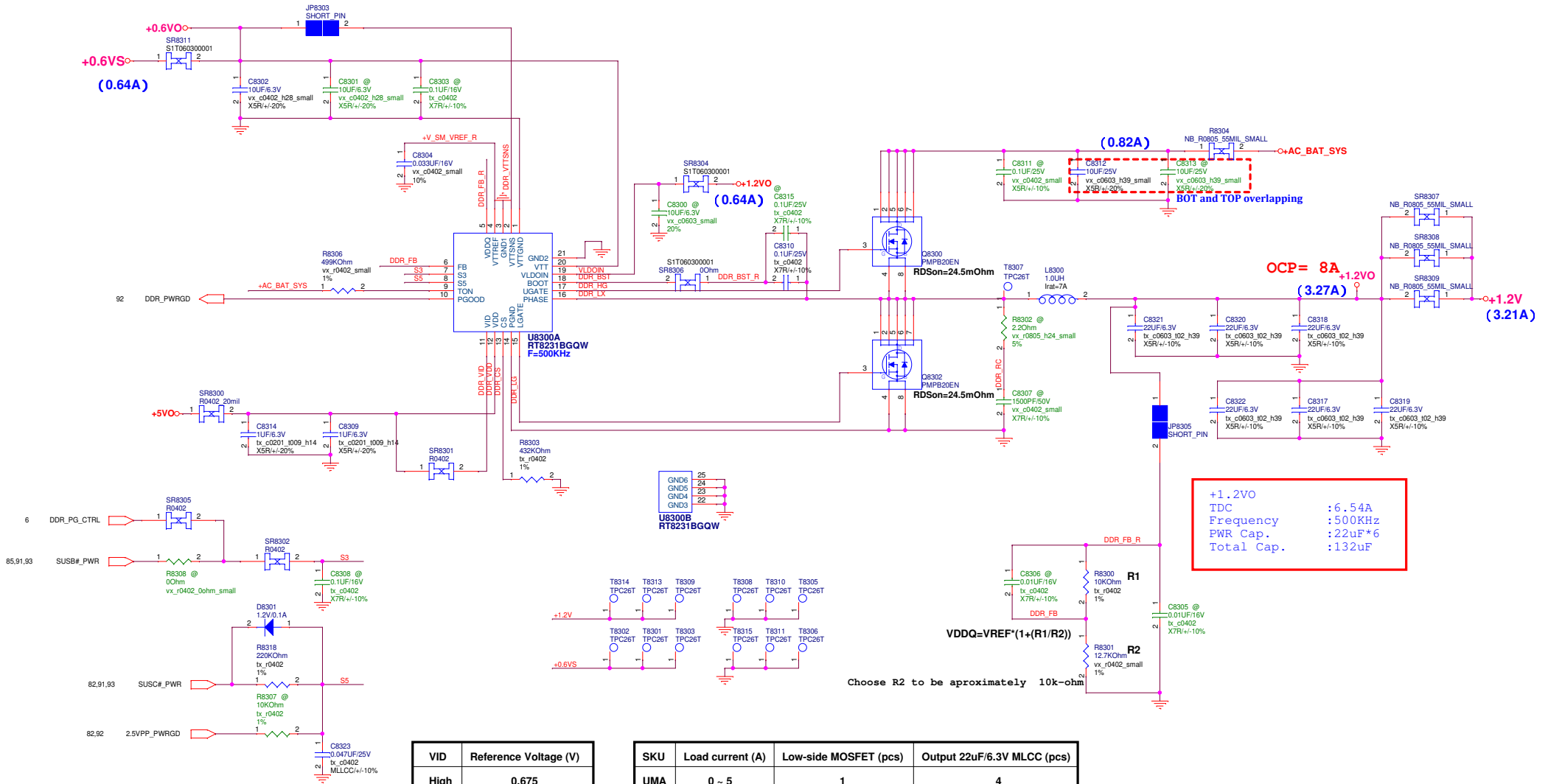
## 1.05VSUS POWER SUPPLY



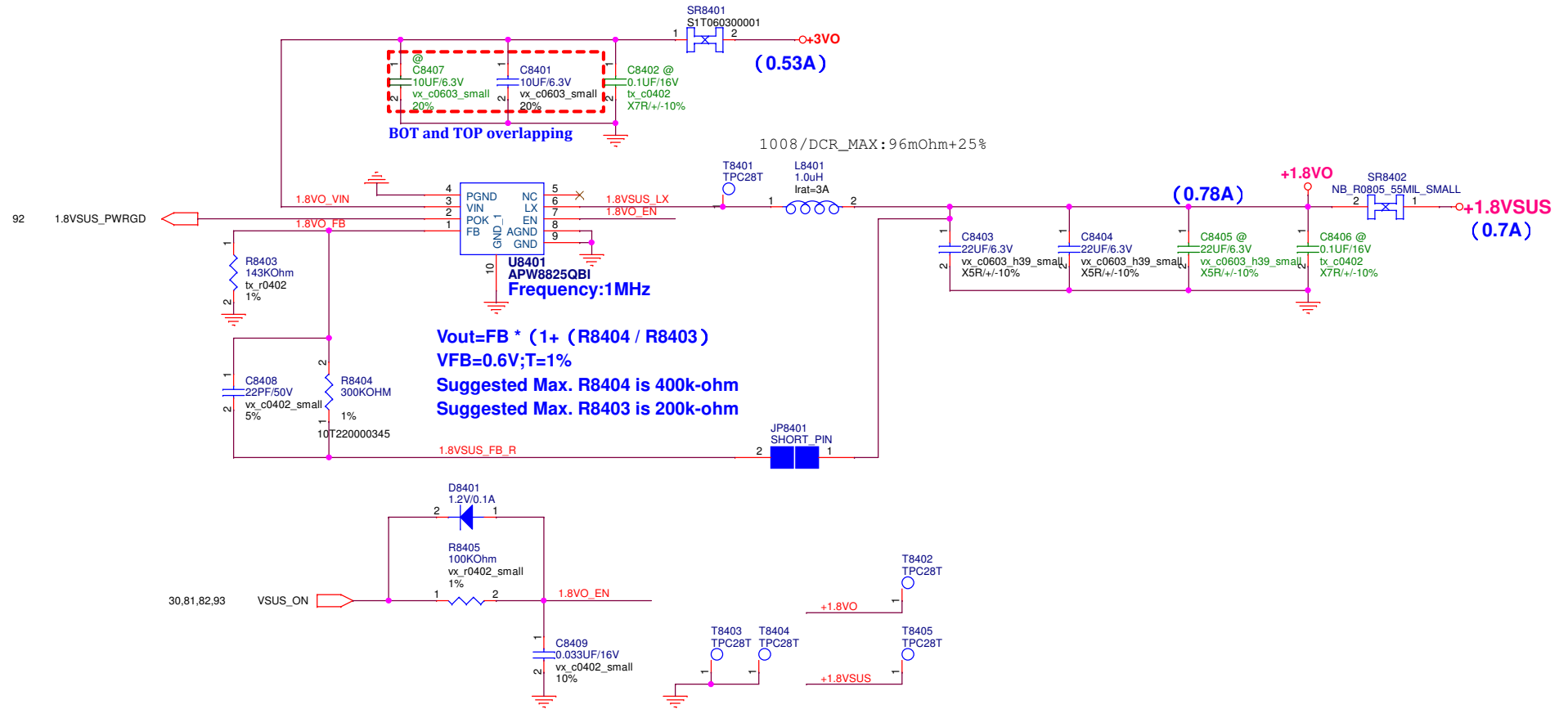
## 2.5V POWER SUPPLY



# DDR & VTT POWER SUPPLY



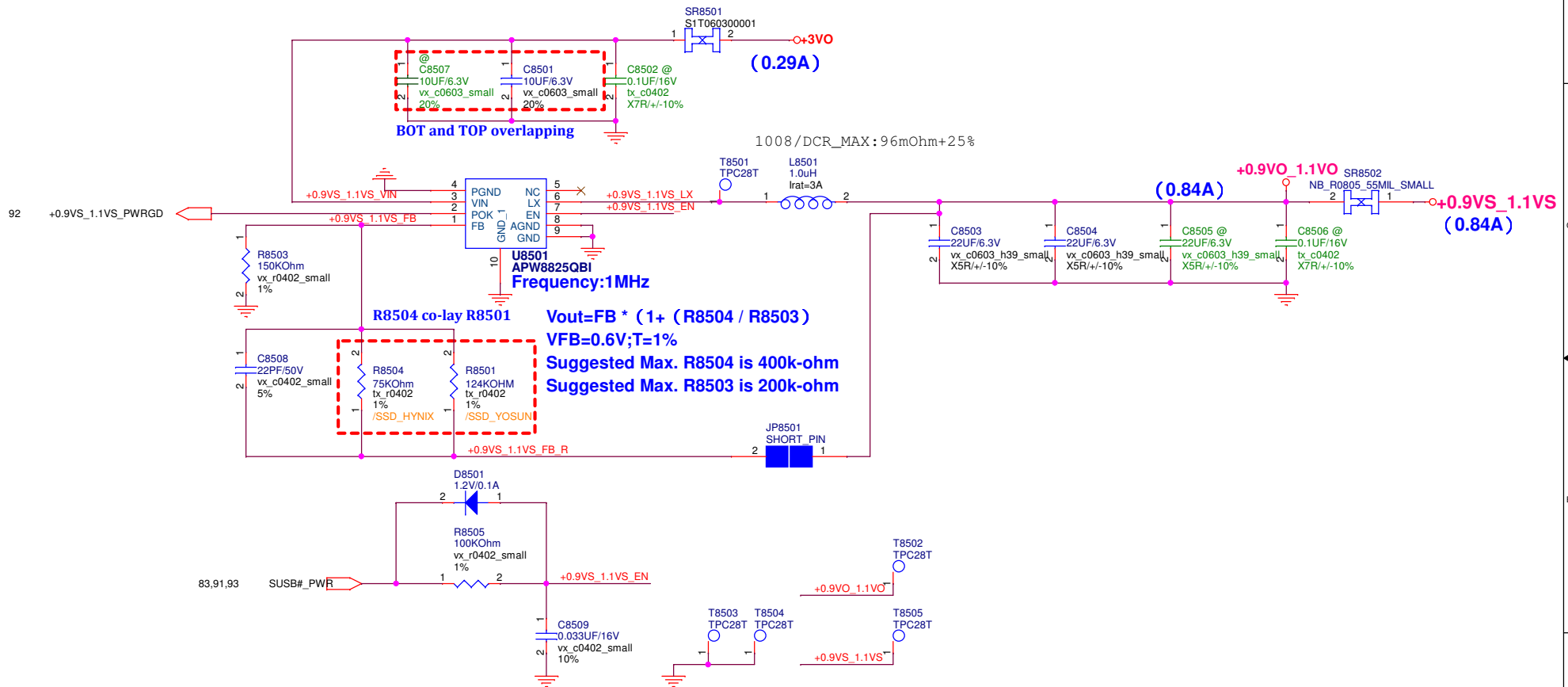
# 1.8VSUS POWER SUPPLY



<Variant Name>

<b>PEGATRON</b>		<b>Title : POWER_+1.8VSUS</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>		<b>Adams Lin</b>	
<b>Size</b>	<b>Project Name</b>	<b>Guinness</b>	<b>Rev</b>
<b>B</b>			<b>1.0</b>
<b>Date:</b> Thursday, September 27, 2018		<b>Sheet</b> 84	<b>of</b> 94

## 0.9V\_1.1VS POWER SUPPLY



<Variant Name>

<b>PEGATRON</b>		<b>POWER_+0.9VS_+1.1VS</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL		Title :	
Engineer:		Adams Lin	
Size B	Project Name	Guinness	Rev 1.0
Date: Thursday, September 27, 2018	Sheet 85	of 94	

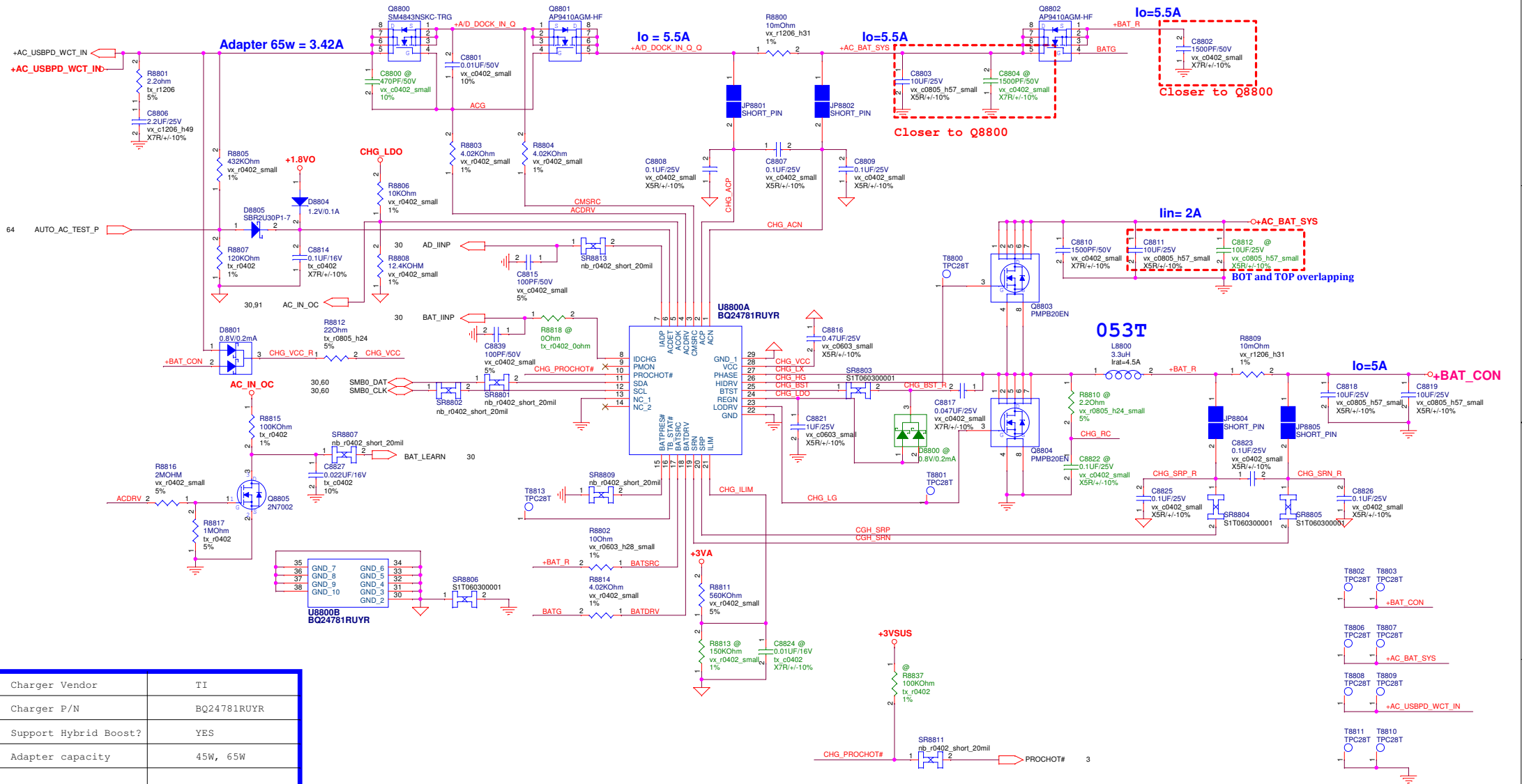
5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
Thursday, September 27, 2018		86	94



## BATTERY CHARGER



Charger Vendor	TI
Charger P/N	BQ24781RUYR
Support Hybrid Boost?	YES
Adapter capacity	45W, 65W
Active/Release Point (Hybrid Boost)	Active - $\geq 1.07 \times CP$ Release - $\leq 0.93 \times CP$
Enable condition Disable condition	RSOC $\geq 40\%$ RSOC $\leq 30\%$

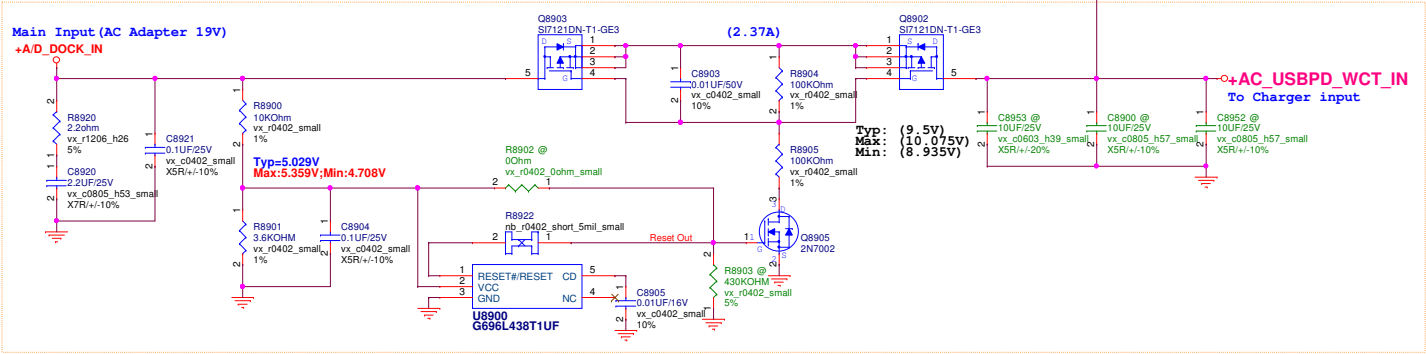
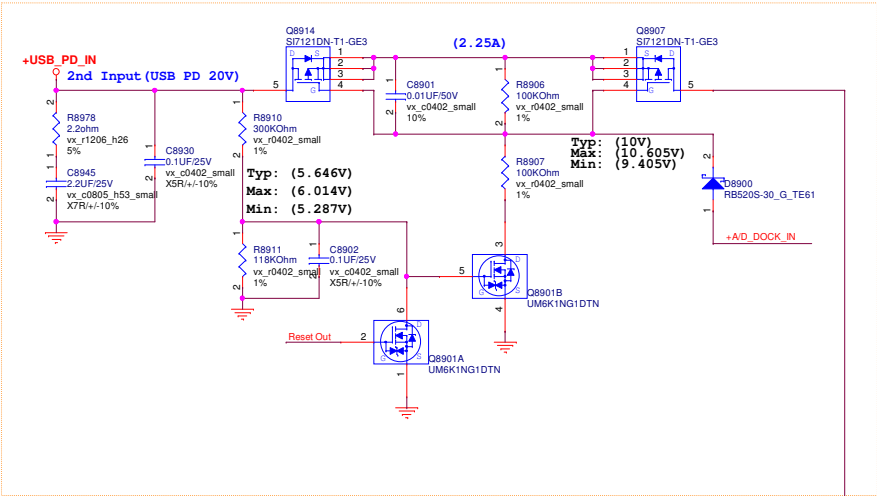
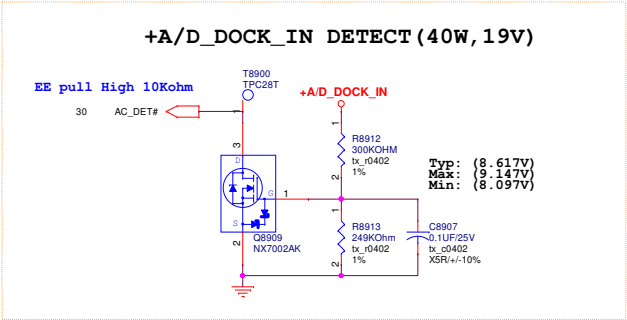
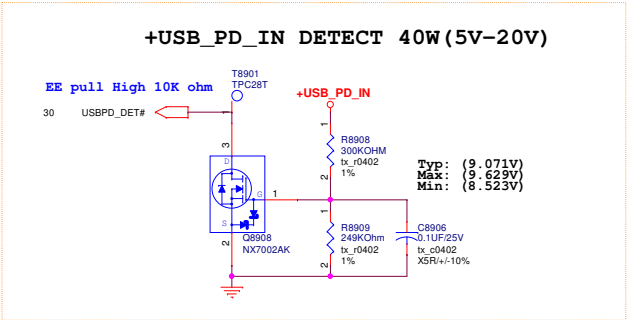
<Variant Name>

<b>PEGATRON</b>		Title : <b>POWER_CHARGER</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
		Engineer: <b>Adams Lin</b>	
Size Custom	Project Name <b>Guinness</b>		Rev 1.0
Date:	<b>Thursday, September 27, 2018</b>	Sheet	<b>88</b> of <b>94</b>



# AC & USB\_PD Detect

# 2 Input switch Circuit

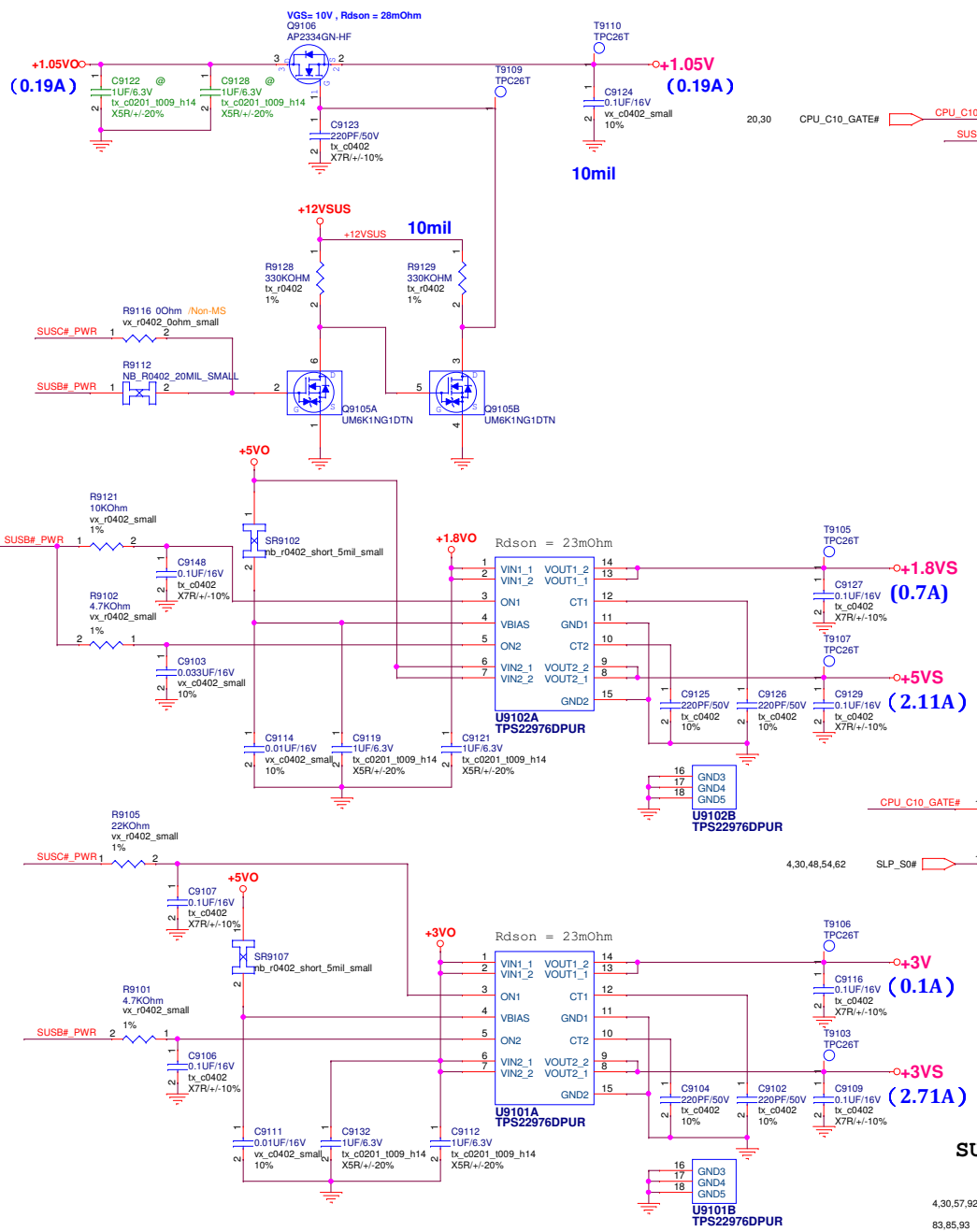


5	4	3	2	1
D				D
C				C
B				B
A				A

<Variant Name>

<b>PEGATRON</b>		<b>Title :</b>	
PEGATRON IS PROPRIETARY AND CONFIDENTIAL			
<b>Engineer:</b>			
Size	Object Name	Rev.	
10	Thursday, September 27, 2018	90	94

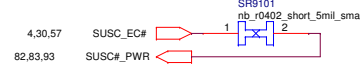
# SUSB#\_PWR POWER SUSC#\_PWR POWER



## SUSB#\_PWR POWER Control



## SUSC#\_PWR POWER Control



<Variant Name>

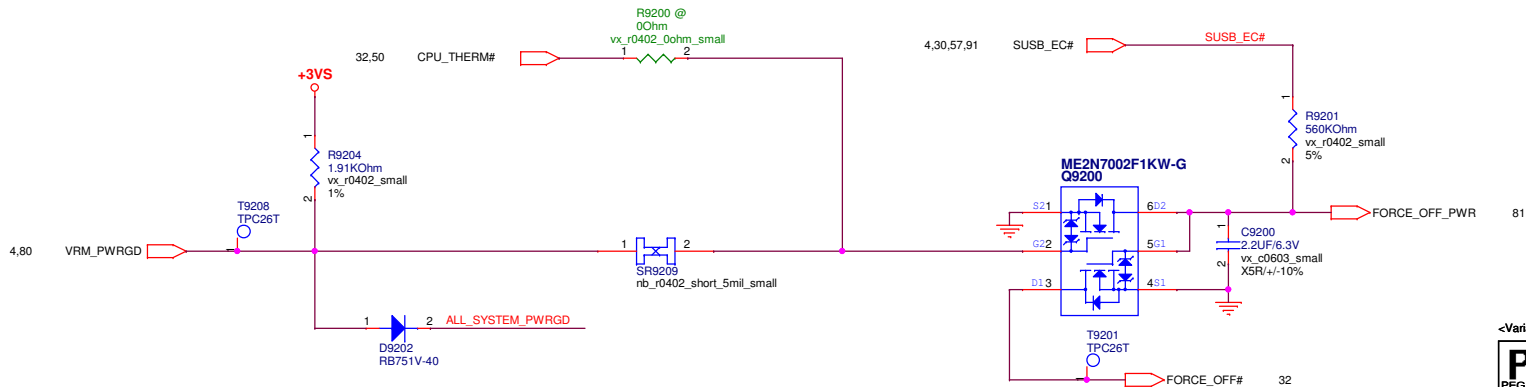
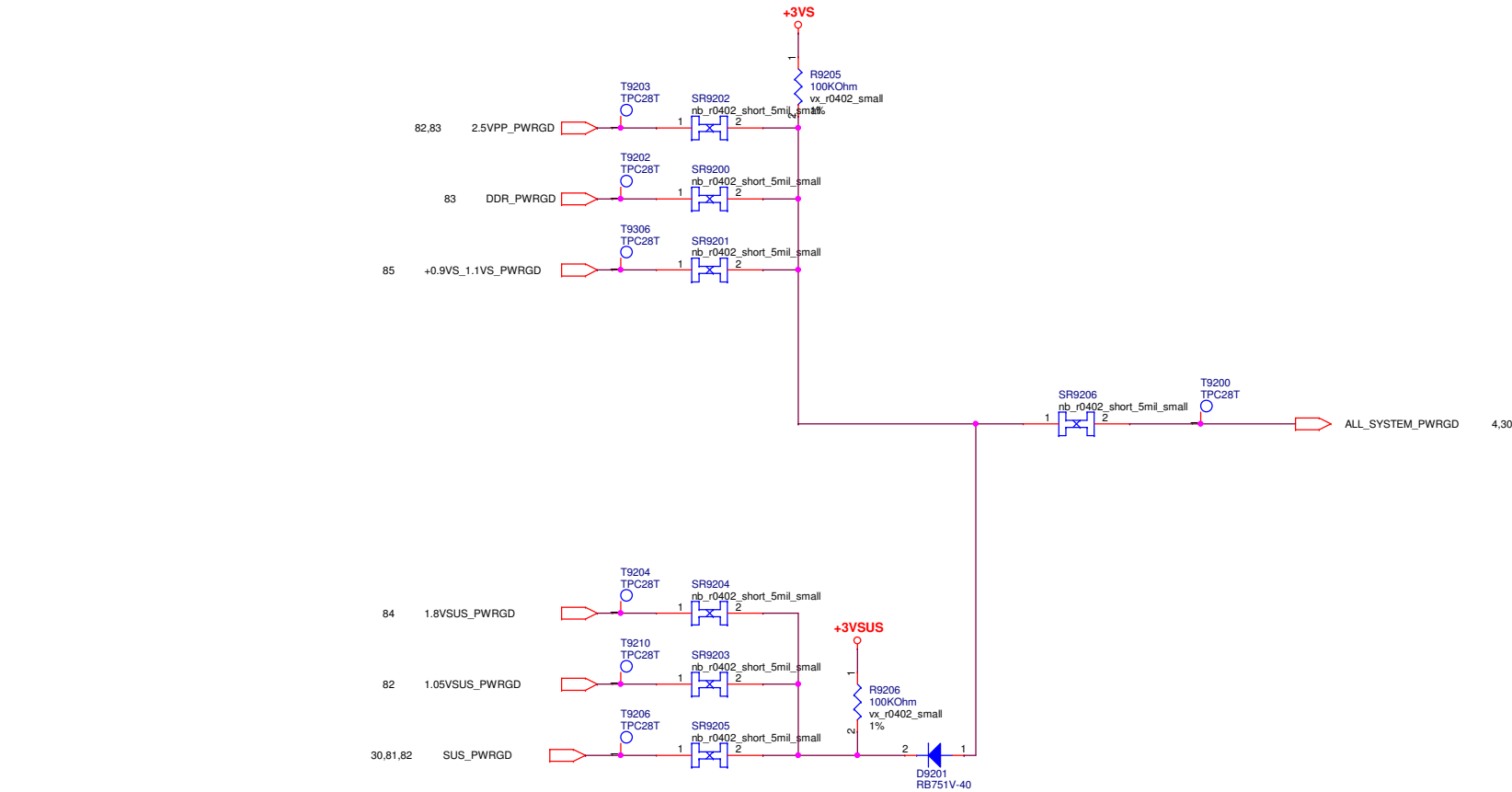
**PEGATRON** Title : POWER\_LOAD SWITCH  
PEGATRON PROPRIETARY AND CONFIDENTIAL

Engineer: **Adams Lin**

Size	Project Name	Rev
Custom	<b>Guinness</b>	1.0

Date: Thursday, September 27, 2018 Sheet 91 of 94

# POWER GOOD DETECTOR



<Variant Name>

<b>PEGATRON</b>		Title : <b>POWER_PROTECT</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
Engineer:		<b>Adams Lin</b>	
Size Custom	Project Name	<b>Guinness</b>	Rev 1.0
Date:	<b>Thursday, September 27, 2018</b>	Sheet	<b>92 of 94</b>

+AC_USBDPD_WCT_IN		+AC_USBDPD_WCT_IN	88,89
+A/D_DOCK_IN		+A/D_DOCK_IN	60,89
+USB_PD_IN		+USB_PD_IN	42,89
+AC_BAT_SYS		+AC_BAT_SYS	41,45,80,81,82,83,88
+BAT_CON		+BAT_CON	60,88
+RTC_POWER		+RTC_POWER	81

+5VA		+5VA	81
+3VA		+3VA	21,30,31,36,57,64,81,88
+5VO		+5VO	81,82,83,91
+3VO		+3VO	81,82,84,85,91
+2.5VO		+2.5VO	82
+1.8VO		+1.8VO	84,88,91

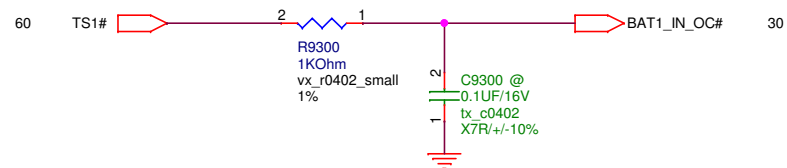
+1.2VO		+1.2VO	83,91
+1.05VO		+1.05VO	82,91
+0.6VO		+0.6VO	83
+12VSUS		+12VSUS	81,91
+5VSUS		+5VSUS	41,42,52,64,81
+3VSUS		+3VSUS	4,10,20,21,22,23,28,30,53,54,62,64,81,88,92
+1.8VSUS		+1.8VSUS	10,21,23,84
+1.05VSUS		+1.05VSUS	10,82

+2P5VPP		+2P5VPP	16,17,57,82
+1.2V		+1.2V	6,9,15,16,17,19,57,83
+1.05V		+1.05V	9,32,57,91

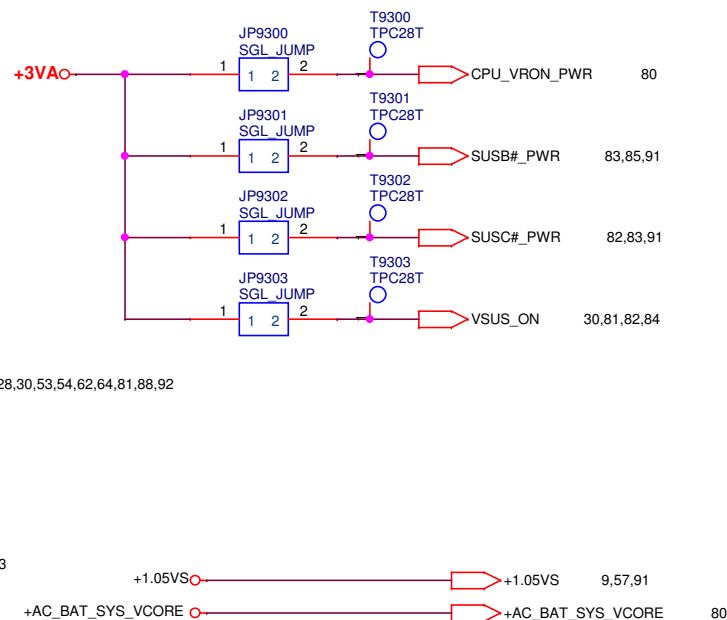
+5VS		+5VS	31,36,48,50,57,69,80,91
+3VS		+3VS	3,4,6,20,21,23,30,32,36,41,42,44,45,48,50,51,53,55,57,62,64,91,92
+1.8VS		+1.8VS	20,36,45,55,57,91
+0.6VS		+0.6VS	15,57,83
+0.9VS_1.1VS		+0.9VS_1.1VS	55,85

+VCORE		+VCORE	8,9,80
+VCCGT		+VCCGT	9,80
+VCCSA		+VCCSA	9,80
+VCCIO		+VCCIO	3,9,57,91
+VCCPRIM_CORE		+VCCPRIM_CORE	10,82

## BATTERY IN DETECT



## FOR POWER TEST



+1.05VS		+1.05VS	9,57,91
+AC_BAT_SYS_VCORE		+AC_BAT_SYS_VCORE	80

<b>PEGATRON</b>		Title : <b>PWR_SIGNAL &amp; DETECT</b>	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
<b>BG1-POWER</b>		Engineer: <b>Adams Lin</b>	
Size Custom	Project Name <b>Guinness</b>		Rev 1.0
Date: <b>Thursday, September 27, 2018</b>		Sheet <b>93</b> of <b>94</b>	

